



	U?A	
F22	TRST	
A24	TCK	
A25	TDI	
B24	TDO	
C24	TMS	W4
		D0
		D1
G22	CLKIN1	D2
K21	CLKIN2	D3
		D4
D5	A0	D5
E8	A1	D6
C4	A2	D7
B4	A3	D8
A4	A4	D9
D7	A5	D10
D8	A6	D11
C6	A7	D12
B5	A8	D13
B6	A9	D14
C7	A10	D15
C8	A11	D16
A6	A12	D17
D9	A13	D18
F11	A14	D19
B7	A15	D20
B8	A16	D21
C9	A17	D22
A7	A18	D23
B9	A19	D24
E11	A20	D25
A8	A21	D26
D11	A22	D27
B10	A23	D28
C11	A24	D29
A9	A25	D30
B11	A26	D31
C12	A27	D32
D12	A28	D33
A10	A29	D34
B12	A30	D35
B13	A31	D36
		D37
AD8	CS9	D38
AE8	CS8	D39
AF8	CS7	D40
AD7	CS6	D41
AC7	CS5	D42
AF7	CS4	D43
AB7	CS3	D44
AE6	CS2	D45
AD6	CS1	D46
AC6	CS0	D47
		D48
C3	TT4	D49
A3	TT3	D50
F8	TT2	D51
B3	TT1	D52
E7	TT0	D53
		D54
E4	TSIZ3	D55
E1	TSIZ2	D56
E2	TSIZ1	D57
E3	TSIZ0	D58
		D59
D1	TS	D60
C19	TRIS	D61
D16	TEA	D62
F5	TBST	D63
Y4	TA	



U?B

		IRQ0/NMI_OUT	A11
		L2_HIT/IRQ4	A17
		CPU_BG/BADDR31/IRQ5/CINT	B14
AB9	LCL_D0/AD0		
AB10	LCL_D1/AD1	CPU_DBG	F13
AC10	LCL_D2/AD2	CPU_BR	B17
AD10	LCL_D3/AD3		
AE10	LCL_D4/AD4	ARTRY	C2
AF10	LCL_D5/AD5	AACK	D3
AF11	LCL_D6/AD6		
AB12	LCL_D7/AD7	BR	C16
AB11	LCL_D8/AD8	BCTL0	AF4
AF12	LCL_D9/AD9		
AE11	LCL_D10/AD10	ABB/IRQ2	C1
AC13	LCL_D11/AD11	BADDR27	C13
AC12	LCL_D12/AD12	BADDR28	A12
AB13	LCL_D13/AD13	CI/BADDR29/IRQ2	D14
AD12	LCL_D14/AD14		
AF14	LCL_D15/AD15	WT/BADDR30/IRQ3	E14
AF17	LCL_D16/AD16	DBB/IRQ3	C15
AE16	LCL_D17/AD17	DBG	A14
AD16	LCL_D18/AD18	ALE	D13
AC16	LCL_D19/AD19	BG	D2
AB16	LCL_D20/AD20		
AF18	LCL_D21/AD21	CS10/BCTL1	AC8
AE17	LCL_D22/AD22	CS11/AP0	AB8
AD17	LCL_D23/AD23		
AB17	LCL_D24/AD24	LWE0/LSDDQM0/LBS0/PCI_CFG0	AC9
AE18	LCL_D25/AD25	LWE1/LSDDQM1/LBS1/PCI_CFG1	AD9
AD18	LCL_D26/AD26	LWE2/LSDDQM2/LBS2/PCI_CFG2	AE9
AC18	LCL_D27/AD27	LWE3/LSDDQM3/LBS3/PCI_CFG3	AF9
AE19	LCL_D28/AD28		
AF20	LCL_D29/AD29	LCL_DP0/C0/BE0	AE12
AD19	LCL_D30/AD30	LCL_DP1/C1/BE1	AA13
AB18	LCL_D31/AD31	LCL_DP2/C2/BE2	AC15
		LCL_DP3/C3/BE3	AF19
		RSTCONF	E24
		SRESET	E23
		QREQ	D18

<Value>

U?C			
AC20	PA0/RESTART1/DREQ3/FCC2_UTM_TXADDR2	GBL/IRQ1	E15
AC21	PA1/REJECT1/DONE3/FCC2_UTM_TXADDR1	HRESET	D24
AF25	PA2/CLK20/DACK3/FCC2_UTM_TXADDR0		
AE24	PA3/CLK19/DACK4/L1RXD1A2/FCC2_UTM_RXADDR0		
AA21	PA4/REJECT2/DONE4/FCC2_UTM_RXADDR1	DP0/RSRV/EXT_BR2	AB3
AD25	PA5/RESTART2/DREQ4/FCC2_UTM_RXADDR2		
AC24	PA6/FCC2_UT_RXADDR3	IRQ1/DP1/EXT_BG2	W5
AA22	PA7/SMSYN2/FCC2_UT_TXADDR3	IRQ2/DP2/TLBISYNC/EXT_DBG2	AC2
AA23	PA8/SMRXD2/FCC2_UT_TXADDR4	IRQ3/DP3/CKSTP_OUT/EXT_BR3	AA3
Y26	PA9/SMTXD2	IRQ4/DP4/CORE_SRESET/EXT_BG3	AD1
W22	PA10/MSNUM5/FCC1_UT8_RXD0/FCC1_UT16_RXD8	IRQ5/CINT/DP5/TBEN/EXT_DBG3	AC1
W23	PA11/MSNUM4/FCC1_UT8_RXD1/FCC1_UT16_RXD9	IRQ6/DP6/CSE0	AB2
V26	PA12/MSNUM3/FCC1_UT8_RXD2/FCC1_UT16_RXD10	IRQ7/DP7/CSE1	Y3
V25	PA13/MSNUM2/FCC1_UT8_RXD3/FCC1_UT16_RXD11		
T22	PA14/FCC1_MII_HDLC_RXD3/FCC1_UT8_RXD4/FCC1_UT16_RXD12	IRQ7/INT_OUT/APE	E5
T25	PA15/FCC1_MII_HDLC_RXD2/FCC1_UT8_RXD5/FCC1_UT16_RXD13		
R24	PA16/FCC1_MII_HDLC_RXD1/FCC1_RMII_RXD1/FCC1_UT8_RXD6/FCC1_UT16_RXD14	LWR	AF6
P22	PA17/FCC_MII_HDLC_RXD0/FCC1_MII_TRAN_RXD/FCC1_RMII_RXD0/FCC1_UT8_RXD7/FCC1_UT16_RXD15		
N26	PA18/FCC1_MII_HDLC_TXD0/FCC1_MII_TRAN_TXD/FCC1_RMII_TXD0/FCC1_UT8_TXD7/FCC1_UT16_TXD15		
N23	PA19/FCC1_MII_HDLC_TXD1/FCC1_RMII_TXD1/FCC1_UT8_TXD6/FCC1_UT16_TXD14		
K26	PA20/FCC1_MII_HDLC_TXD2/FCC1_UT8_TXD5/FCC1_UT16_TXD13	PCI_MODE	AD24
L23	PA21/FCC1_MII_HDLC_TXD3/FCC1_UT8_TXD4/FCC1_UT16_TXD12		
K23	PA22/FCC1_UT8_TXD3/FCC1_UT16_TXD11	LSDA10/LGPL0/PCI_MODCKH0	AB6
H26	PA23/FCC1_UT8_TXD2/FCC1_UT16_TXD10	LSDWE/LGPL1/PCI_MODCKH1	AF5
F25	PA24/MSNUM1/FCC1_UT8_TXD1/FCC1_UT16_TXD9	LOE/LSDRAS/LGPL2/PCI_MODCKH2	AE5
D26	PA25/MSNUM0/FCC1_UT8_TXD0/FCC1_UT16_TXD8	LSDCAS/LGPL3/PCI_MODCKH3	AD5
D25	PA26/FCC1_MII_RMII_RX_ER/FCC1_UTM_RXCLAV/FCC1_UTS_RXCLAV		
C25	PA27/FCC1_MII_RX_DV/FCC1_RMII_CRS_DV/FCC1_UT_RXSOC		
C22	PA28/FCC1_MII_TX_EN/FCC1_RMII_TX_EN/FCC1_UTM_RXENB/FCC1_UTS_RXENB	LGTA/LUPMWAIT/LGPL4/LPBS	AC5
B21	PA29/FCC1_MII_TX_ER/FCC1_UT_RXSOC	LGPL5/LSDAMUX/PCI_MODCK	AB5
A20	PA30/FCC1_MII_CRS/FCC1_RTS/FCC1_UTM_TXCLAV/FCC1_UTS_TXCLAV		
A19	PA31/FCC1_MII_COL/FCC1_UTM_TXENB/FCC1_UTS_TXENB		
AD21	PB4/FCC3_MII_HDLC_TXD3/L1RSYNCA2/FCC3_RTS/FCC2_UT8_RXD0	PSDA10/PGPL0	AE2
AD22	PB5/FCC3_MII_HDLC_TXD2/L1TSYNCA2/L1GNTA2/FCC2_UT8_RXD1	PSDWE/PGPL1	AD2
AC22	PB6/FCC3_MII_HDLC_TXD1/FCC3_RMII_TXD1/L1RXDA2/L1RXD0A2/FCC2_UT8_RXD2	POE/PSDRAS/PGPL2	AE1
AE26	PB7/FCC3_MII_HDLC_TXD0/FCC3_RMII_TXD0/FCC3_TXD/L1TXDA2/L1TXD0A2/FCC2_UT8_RXD3	PSDCAS/PGPL3	AC3
AB23	PB8/FCC3_MII_HDLC_RXD0/FCC3_RMII_RXD0/FCC3_RXD/TXD3/FCC2_UT8_TXD3	PGTA/PUPMWAIT/PGPL4/PPBS	W6
AC26	PB9/FCC3_MII_HDLC_RXD1/FCC3_RMII_RXD1/L1TXD2A2/FCC2_UT8_TXD2	PSDAMUX/PGPL5	AA4
AB26	PB10/FCC3_MII_HDLC_RXD2/FCC2_UT8_TXD1		
AA25	PB11/FCC3_MII_HDLC_RXD3/FCC2_UT8_TXD0		
W26	PB12/FCC3_MII_CRS/TXD2	PSDVAL	D15
W25	PB13/FCC3_MII_COL/L1TXD1A2		
V24	PB14/FCC3_MII_RMII_TX_EN/RXD3	PWE0/PSDDQM0/PBS0	AA5
U24	PB15/FCC3_MII_TX_ER/RXD2	PWE1/PSDDQM1/PBS1	AE4
R22	PB16/FCC3_MII_RMII_RX_ER/CLK18	PWE2/PSDDQM2/PBS2	AD4
R23	PB17/FCC3_MII_RX_DV/CLK17/FCC3_RMII_CRS_DV	PWE3/PSDDQM3/PBS3	AF3
M23	PB18/FCC2_MII_HDLC_RXD3/L1CLKOD2/L1RXD2A2/FCC2_UT8_RXD4	PWE4/PSDDQM4/PBS4	AB4
L24	PB19/FCC2_MII_HDLC_RXD2/L1RQD2/L1RXD3A2/FCC2_UT8_RXD5	PWE5/PSDDQM5/PBS5	AE3
K24	PB20/FCC2_MII_HDLC_RMII_RXD1/L1RSYNCD2/FCC2_UT8_RXD6	PWE6/PSDDQM6/PBS6	AF2
L21	PB21/FCC2_MII_HDLC_RMII_RXD0/FCC2_TRAN_RXD/L1TSYNCD2/L1GNTD2/FCC2_UT8_RXD7	PWE7/PSDDQM7/PBS7	AD3
P25	PB22/FCC2_MII_HDLC_RMII_TXD0/FCC2_TXD/FCC2_RMII_TXD0/L1RXDD2/FCC2_UT8_TXD7		
N25	PB23/FCC2_MII_HDLC_TXD1/L1RXD2A1/L1TXDD2/FCC2_RMII_TXD1/FCC2_UT8_TXD6		
E26	PB24/FCC2_MII_HDLC_TXD2/L1RSYNCC2/FCC2_UT8_TXD5	MODCK1/AP1/TC0/BNKSEL0	B16
H23	PB25/FCC2_MII_HDLC_TXD3/L1TSYNCC2/L1GNTC2/FCC2_UT8_TXD4	MODCK2/AP2/TC1/BNKSEL1	F16
C26	PB26/FCC2_MII_CRS/L1RXDC2/FCC2_UT8_TXD1	MODCK3/AP3/TC2/BNKSEL2	A15
B26	PB27/FCC2_MII_COL/L1TXDC2/FCC2_UT8_TXD0		
A22	PB28/FCC2_MII_RX_ER/FCC2_RMII_RX_ER/FCC2_RTS/L1TSYNCB2/L1GNTB2/TXD1		
A21	PB29/L1RSYNCB2/FCC2_MII_TX_EN/FCC2_RMII_TX_EN/FCC2_UTM_RXCLAV/FCC2_UTS_RXCLAV		
E20	PB30/FCC2_MII_RX_DV/L1RXDB2/FCC2_RMII_CRS_DV/FCC2_UT_RXSOC		
C20	PB31/FCC2_MII_TX_ER/L1TXDB2/FCC2_UT_RXSOC		

<Value>



U?D		
AE22	PC0/DREQ1/BRGO7/SMSYN2/ L1CLKOA2	
AA19	PC1/DREQ2/SPISEL/BRGO6/L1RQA2	
AF24	PC2/FCC3_CD/DONE2/FCC2_UT8_TXD3	
AE25	PC3/FCC3_CTS/DACK2/CTS4/USB_RP/FCC2_UT8_TXD2	PORESET
AB22		
AC25	PC4/SI2_L1ST4/FCC2_CD/FCC2_UTM_RXENB/FCC2_UTS_RXENB	
AB25	PC5/SI2_L1ST3/FCC2_CTS/FCC2_UTM_TXCLAV/FCC2_UTS_TXCLAV	L_A14/PAR
AA24	PC6/FCC1_CD/FCC1_UTM_RXADDR2/FCC1_UTS_RXADDR2/FCC1_UTM_RXCLAV1	
Y24	PC7/FCC1_CTS/FCC1_UTM_TXADDR2/FCC1_UTS_TXADDR2/FCC1_UTM_TXCLAV1	L_A15/FRAME/SMI
U22	PC8/CD4/RENA4/SI2_L1ST2/CTS3/USB_RN/FCC1_UT16_TXD0	
V23	PC9/CTS4/CLSN4/SI2_L1ST1/ L1TSYNCA2/L1GNTA2/USB_RP/FCC1_UT16_TXD1	L_A16/TRDY
U23	PC10/CD3/RENA3/FCC1_UT16_TXD2/FCC2_UT8_RXD3	
T26	PC11/CTS3/CLSN3/L1TXD3A2/FCC2_UT8_RXD2	L_A17/IRDY/CKSTP_OUT
R26	PC12/CD2/RENA2/FCC1_UTM_RXADDR1/FCC1_UTS_RXADDR1	
P26	PC13/CTS2/CLSN2/FCC1_UTM_TXADDR1/FCC1_UTS_TXADDR1	L_A18/STOP
P24	PC14/CD1/RENA1/FCC1_UTM_RXADDR0/FCC1_UTS_RXADDR0	
M26	PC15/CTS1/CLSN1/SMTXD2/FCC1_UTM_TXADDR0/FCC1_UTS_TXADDR0	L_A19/DEVSEL
L26	PC16/CLK16/TIN4	
M24	PC17/CLK15/TIN3/BRGO8	L_A20/IDSEL
L22	PC18/CLK14/TGATE2	
K25	PC19/CLK13/BRGO7/SPICLK	L_A21/PERR
J25	PC20/CLK12/TGATE1/USB_OE	
G26	PC21/CLK11/BRGO6	L_A22/SERR
F26	PC22/CLK10/DONE1/FCC1_UT_TXPRTY	
G24	PC23/CLK9/BRGO5/DACK1	L_A23/REQ0
E25	PC24/CLK8/TOUT4/FCC2_UT8_TXD3	
G23	PC25/CLK7/BRGO4/FCC2_UT8_TXD2	L_A24/REQ1/HSEJSW
B23	PC26/CLK6/TOUT3/TMCLK	
E22	PC27/FCC3_TXD/FCC3_MII_TXD0/FCC3_RMII_TXD0/CLK5/BRGO3	L_A25/GNT0
E21	PC28/CLK4/TIN1/TOUT2/CTS2/CLSN2/FCC2_UT_RXADDR4	
D21	PC29/CLK3/TIN2/BRGO2/CTS1/CLSN1	L_A26/GNT1/HSLED
B20	PC30/CLK2/TOUT1/FCC2_UT8_TXD3	
	PC31/CLK1/BRGO1	L_A27/GNT2/HSENUM
AF23	PD4/BRGO8/FCC3_RTS/SMRXD2	L_A28/RST/CORE_SRESET
AE23	PD5/DONE1/FCC1_UT16_TXD3	
AB21	PD6/DACK1/FCC1_UT16_TXD4	L_A29/INTA
AD23	PD7/SMSYN1/FCC1_TXCLAV2/FCC1_UTM_TXADDR3/FCC1_UTS_TXADDR3/FCC2_UTM_TXADDR4/FCC2_UTS_TXADDR1	
AD26	PD8/SMRXD1/BRGO5/FCC2_UT_TXPRTY	L_A30/REQ2
Y22	PD9/SMTXD1/BRGO3/FCC2_UT_RXPRTY	
AB24	PD10/L1CLKOB2/BRGO4/FCC2_UT8_RXD1	L_A31/DLLOUT
Y23	PD11/L1RQB2/FCC2_UT8_RXD0/L1GNTB1	
AA26	PD12	
W24	PD13	
V22	PD14/L1CLKOC2/I2CSCL/FCC1_UT16_RXD0	NC1
U26	PD15/L1RQC2/I2CSDA/FCC1_UT16_RXD1	NC2
T23	PD16/SPIMISO/FCC1_UT_TXPRTY	
R25	PD17/BRGO2/SPIMOSI/FCC1_UT_RXPRTY	
P23	PD18/SPICLK/FCC1_UTM_RXADDR4/FCC1_UTS_RXADDR4/FCC1_UTM_RXCLAV3/FCC2_UTM_RXADDR3/FCC2_UTS_RXADDR0	
N22	PD19/SPISEL/BRGO1/FCC1_UTM_TXADDR4/FCC1_UTS_TXADDR4/FCC1_UTM_TXCLAV3/FCC2_UTM_TXADDR3/FCC2_UTS_TXADDR0	
M25	PD20/RTS4/TENA4/L1RSYNCA2/USB_TP/FCC1_UT16_RXD2	
L25	PD21/TXD4/L1RXD0A2/L1RXDA2/USB_TN/FCC1_UT16_RXD3	
J26	PD22/RXD4/L1TXD0A2/L1TXDA2/USB_RXD/FCC1_UT16_TXD5	
K22	PD23/RTS3/TENA3/FCC1_UT16_RXD4	
G25	PD24/TXD3/FCC1_UT16_RXD5	SPARE4
H24	PD25/RXD3/FCC1_UT16_TXD6	SPARE6
F24	PD26/RTS2/TENA2/FCC1_UT16_RXD6	
H22	PD27/TXD2/FCC1_UT16_RXD7	
B22	PD28/RXD2/FCC1_UT16_TXD7	
D22	PD29/RTS1/TENA1/FCC1_UTM_RXADDR3/FCC1_UTS_RXADDR3/FCC1_UTM_RXCLAV2/FCC2_UTM_RXADDR4/FCC2_UTS_RXADDR1	
C21	PD30/TXD1/FCC2_UTM_TXENB/FCC2_UTS_TXENB	
E19	PD31/RXD1	

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U?E

D19	VCCSYN	VDDH1	E6
		VDDH2	F6
		VDDH3	H6
		VDDH4	L5
K6	VCCSYN1	VDDH5	L6
		VDDH6	P6
		VDDH7	T6
		VDDH8	U6
L3 V4 W3 AC11 AD11 AB15 U25 T24 J24 H25 F23 B19 D17 C17 D10 C10	VDD1 VDD2 VDD3 VDD4 VDD5 VDD6 VDD7 VDD8 VDD9 VDD10 VDD11 VDD12 VDD13 VDD14 VDD15 VDD16	VDDH9	V5
		VDDH10	Y5
		VDDH11	AA6
		VDDH12	AA8
		VDDH13	AA10
		VDDH14	AA11
		VDDH15	AA14
		VDDH16	AA16
		VDDH17	AA17
		VDDH18	AB19
		VDDH19	AB20
		VDDH20	W21
		VDDH21	U21
		VDDH22	T21
		VDDH23	P21
		VDDH24	N21
		VDDH25	M22
		VDDH26	J22
		VDDH27	H21
		VDDH28	F21
		VDDH29	F19
		VDDH30	F17
		VDDH31	E16
		VDDH32	F14
		VDDH33	E13
		VDDH34	E12
		VDDH35	F10
		VDDH36	E10
		VDDH37	E9

<Value>



U?F

A2			M14
B1	GND1	GND46	M15
B2	GND2	GND47	M16
A5	GND3	GND48	M17
C5	GND4	GND49	N10
C18	GND5	GND50	N11
D4	GND6	GND51	N12
D6	GND7	GND52	N13
G2	GND8	GND53	N14
L4	GND9	GND54	N15
P1	GND10	GND55	N16
R1	GND11	GND56	N17
R4	GND12	GND57	P10
AC4	GND13	GND58	P11
AE7	GND14	GND59	P12
AC23	GND15	GND60	P13
Y25	GND16	GND61	P14
N24	GND17	GND62	P15
J23	GND18	GND63	P16
A23	GND19	GND64	P17
D23	GND20	GND65	R10
D20	GND21	GND66	R11
E18	GND22	GND67	R12
A13	GND23	GND68	R13
A16	GND24	GND69	R14
K10	GND25	GND70	R15
K11	GND26	GND71	R16
K12	GND27	GND72	R17
K13	GND28	GND73	T10
K14	GND29	GND74	T11
K15	GND30	GND75	T12
K16	GND31	GND76	T13
K17	GND32	GND77	T14
L10	GND33	GND78	T15
L11	GND34	GND79	T16
L12	GND35	GND80	T17
L13	GND36	GND81	U10
L14	GND37	GND82	U11
L15	GND38	GND83	U12
L16	GND39	GND84	U13
L17	GND40	GND85	U14
M10	GND41	GND86	U15
M11	GND42	GND87	U16
M12	GND43	GND88	U17
M13	GND44	GND89	A18
	GND45	GND90	B18
		GND91	

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