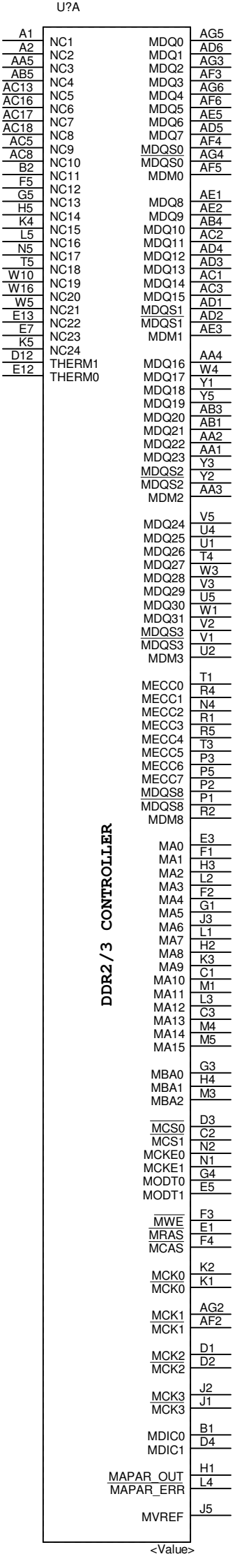
U?A
A1
A2
AA5
AB5
AC13
AC16
AC17
AC18
AC5
AC8
B2
F5
G5
H5
K4
L5
N5
T5
W10
W16
W5
E13
E7
K5
D12
E12
NC1
NC2
NC3
NC4
NC5
NC6
NC7
NC8
NC9
NC10
NC11
NC12
NC13
NC14
NC15
NC16
NC17
NC18
NC19
NC20
NC21
NC22
NC23
NC24
THERM1
THERM0
DDR2/3 CONTROLLER
MDQ0
MDQ1
MDQ2
MDQ3
MDQ4
MDQ5
MDQ6
MDQ7
MDQS0
MDQS0
MDM0
MDQ8
MDQ9
MDQ10
MDQ11
MDQ12
MDQ13
MDQ14
MDQ15
MDQS1
MDQS1
MDM1
MDQ16
MDQ17
MDQ18
MDQ19
MDQ20
MDQ21
MDQ22
MDQ23
MDQS2
MDQS2
MDM2
MDQ24
MDQ25
MDQ26
MDQ27
MDQ28
MDQ29
MDQ30
MDQ31
MDQS3
MDQS3
MDM3
MECC0
MECC1
MECC2
MECC3
MECC4
MECC5
MECC6
MECC7
MDQS8
MDQS8
MDM8
MA0
MA1
MA2
MA3
MA4
MA5
MA6
MA7
MA8
MA9
MA10
MA11
MA12
MA13
MA14
MA15
MBA0
MBA1
MBA2
MCS0
MCS1
MCKE0
MCKE1
MODT0
MODT1
MWE
MRAS
MCAS
MCK0
MCK0
MCK1
MCK1
MCK2
MCK2
MCK3
MCK3
MDIC0
MDIC1
MAPAR_OUT
MAPAR_ERR
MVREF
AG5
AD6
AG3
AF3
AG6
AF6
AE5
AD5
AF4
AG4
AF5
AE1
AE2
AB4
AC2
AD4
AD3
AC1
AC3
AD1
AD2
AE3
AA4
W4
Y1
Y5
AB3
AB1
AA2
AA1
Y3
Y2
AA3
V5
U4
U1
T4
W3
V3
U5
W1
V2
V1
U2
T1
R4
N4
R1
R5
T3
P3
P5
P2
P1
R2
E3
F1
H3
L2
F2
G1
J3
L1
H2
K3
C1
M1
L3
C3
M4
M5
G3
H4
M3
D3
C2
N2
N1
G4
E5
F3
E1
F4
K2
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AG2
AF2
D1
D2
J2
J1
B1
D4
H1
L4
J5
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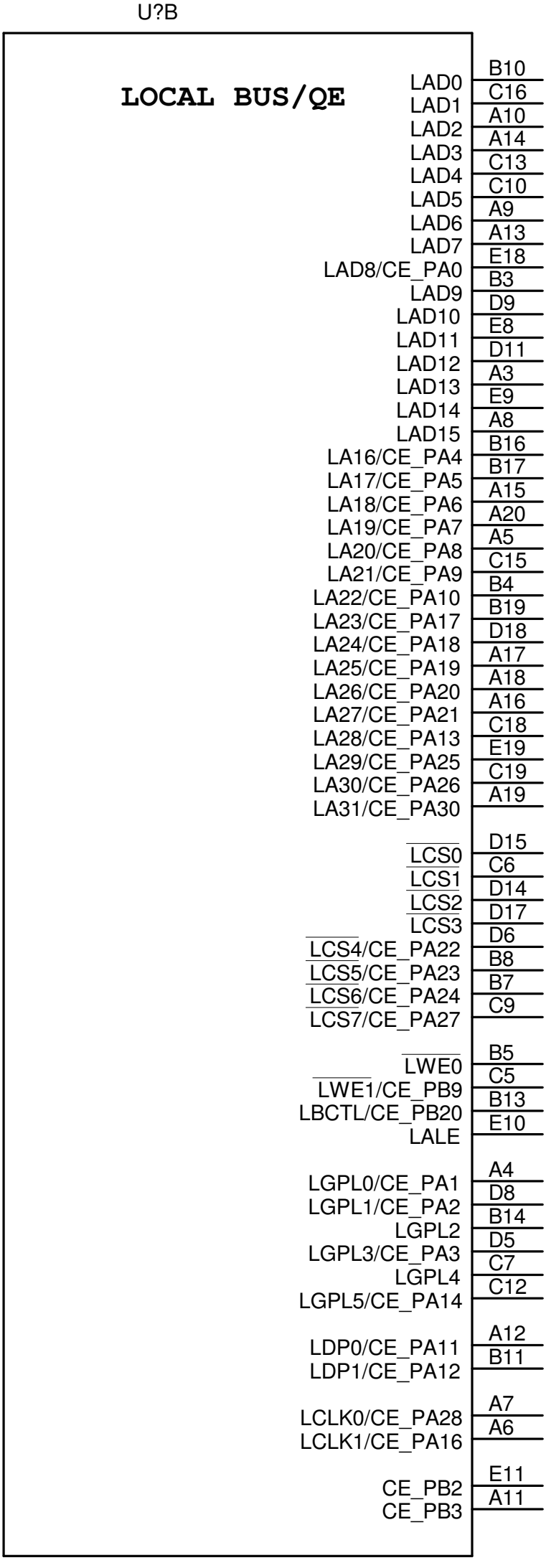
U?B
LOCAL BUS/QE
LAD0 B10
LAD1 C16
LAD2 A10
LAD3 A14
LAD4 C13
LAD5 C10
LAD6 A9
LAD7 A13
LAD8/CE_PA0 E18
LAD9 B3
LAD10 D9
LAD11 E8
LAD12 D11
LAD13 A3
LAD14 E9
LAD15 A8
LA16/CE_PA4 B16
LA17/CE_PA5 B17
LA18/CE_PA6 A15
LA19/CE_PA7 A20
LA20/CE_PA8 A5
LA21/CE_PA9 C15
LA22/CE_PA10 B4
LA23/CE_PA17 B19
LA24/CE_PA18 D18
LA25/CE_PA19 A17
LA26/CE_PA20 A18
LA27/CE_PA21 A16
LA28/CE_PA13 C18
LA29/CE_PA25 E19
LA30/CE_PA26 C19
LA31/CE_PA30 A19
LCS0 D15
LCS1 C6
LCS2 D14
LCS3 D17
LCS4/CE_PA22 D6
LCS5/CE_PA23 B8
LCS6/CE_PA24 B7
LCS7/CE_PA27 C9
LWE0 B5
LWE1/CE_PB9 C5
LBCTL/CE_PB20 B13
LALE E10
LGPL0/CE_PA1 A4
LGPL1/CE_PA2 D8
LGPL2 B14
LGPL3/CE_PA3 D5
LGPL4 C7
LGPL5/CE_PA14 C12
LDP0/CE_PA11 A12
LDP1/CE_PA12 B11
LCLK0/CE_PA28 A7
LCLK1/CE_PA16 A6
CE_PB2 E11
CE_PB3 A11
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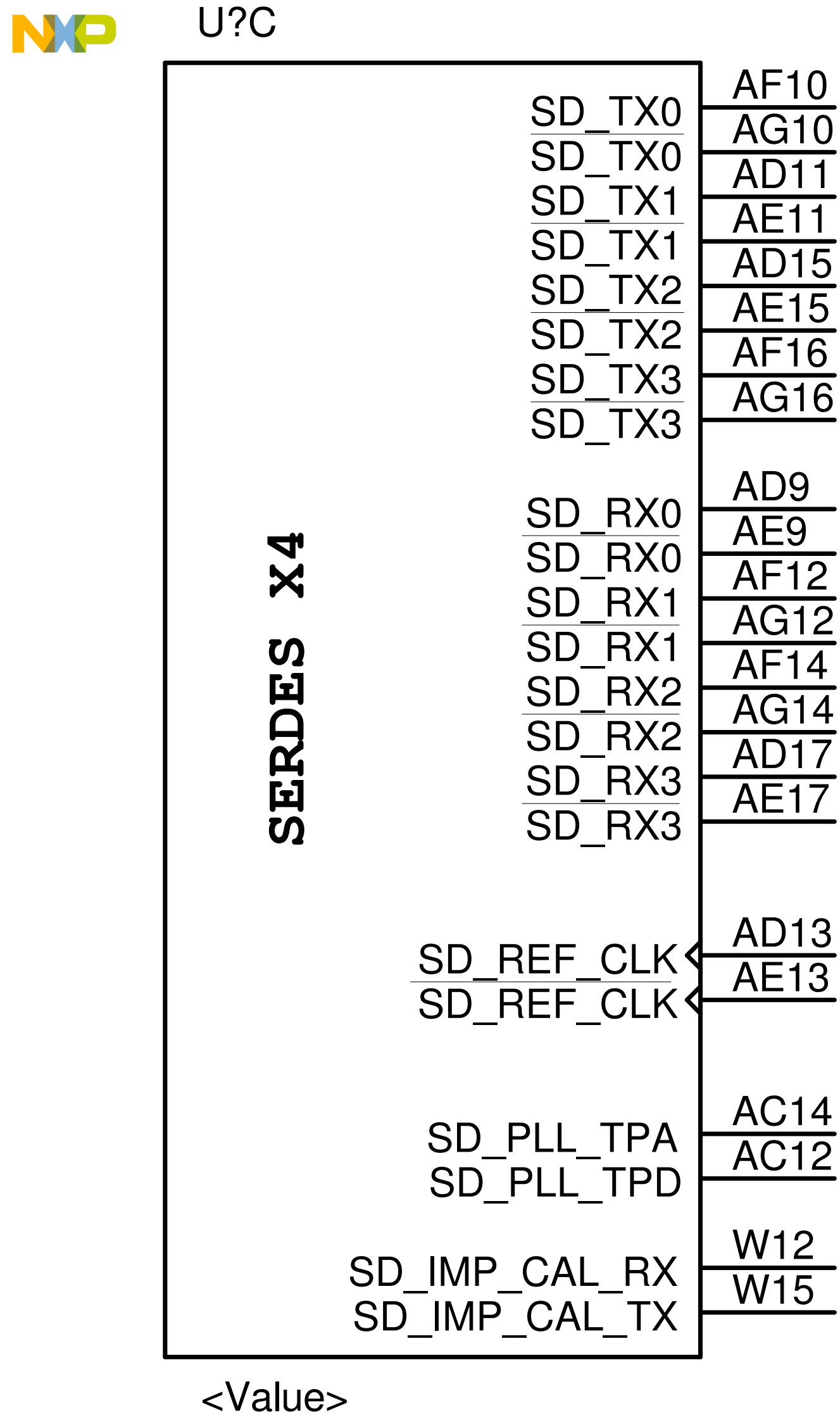
U?C
SERDES X4
SD_TX0
SD_TX0
SD_TX1
SD_TX1
SD_TX2
SD_TX2
SD_TX3
SD_TX3
AF10
AG10
AD11
AE11
AD15
AE15
AF16
AG16
SD_RX0
SD_RX0
SD_RX1
SD_RX1
SD_RX2
SD_RX2
SD_RX3
SD_RX3
AD9
AE9
AF12
AG12
AF14
AG14
AD17
AE17
SD_REF_CLK
SD_REF_CLK
AD13
AE13
SD_PLL_TPA
SD_PLL_TPD
AC14
AC12
SD_IMP_CAL_RX
SD_IMP_CAL_TX
W12
W15
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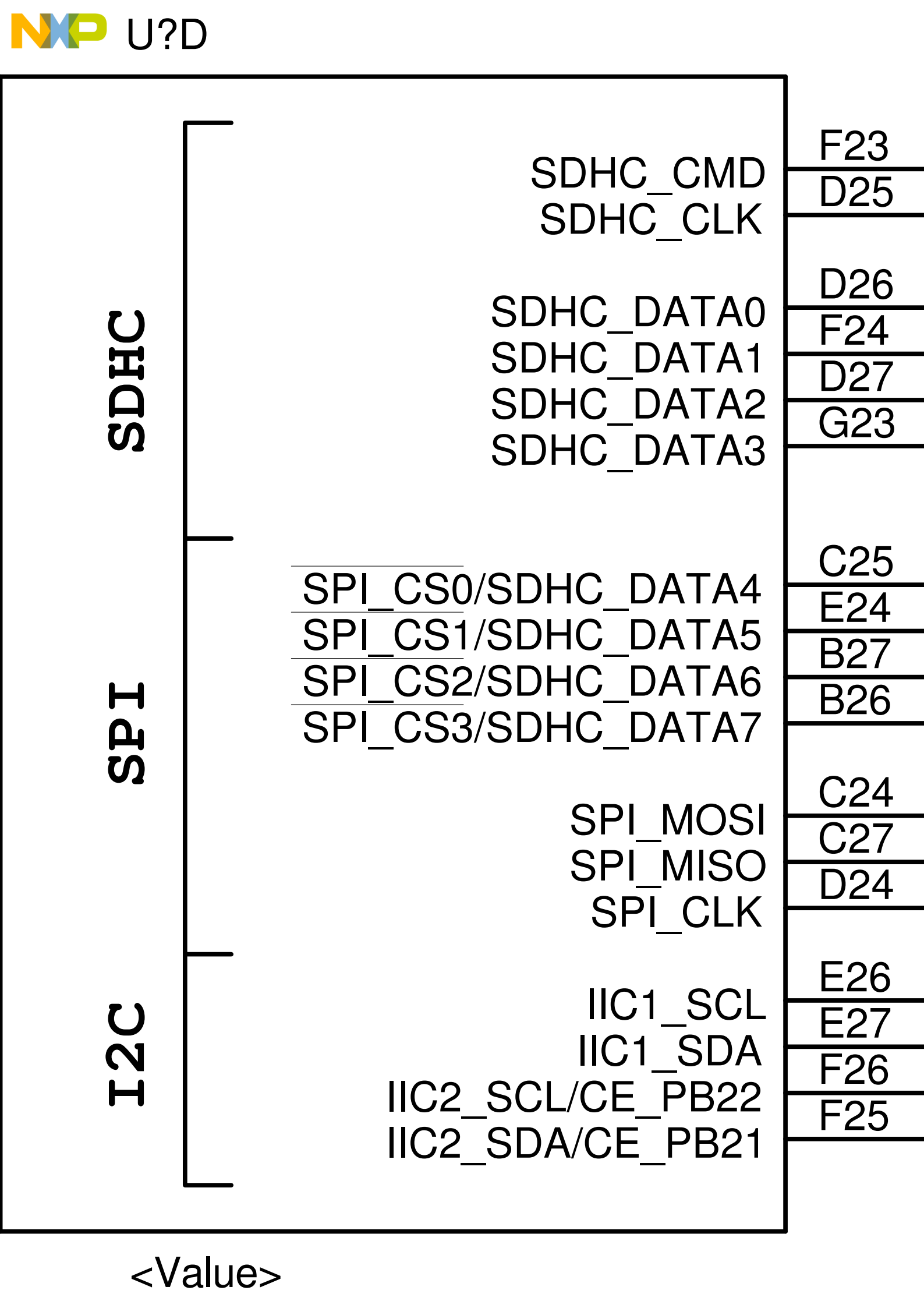
U?D
SDHC
SPI
I2C
SDHC_CMD
SDHC_CLK
SDHC_DATA0
SDHC_DATA1
SDHC_DATA2
SDHC_DATA3
SPI_CS0/SDHC_DATA4
SPI_CS1/SDHC_DATA5
SPI_CS2/SDHC_DATA6
SPI_CS3/SDHC_DATA7
SPI_MOSI
SPI_MISO
SPI_CLK
IIC1_SCL
IIC1_SDA
IIC2_SCL/CE_PB22
IIC2_SDA/CE_PB21
F23
D25
D26
F24
D27
G23
C25
E24
B27
B26
C24
C27
D24
E26
E27
F26
F25
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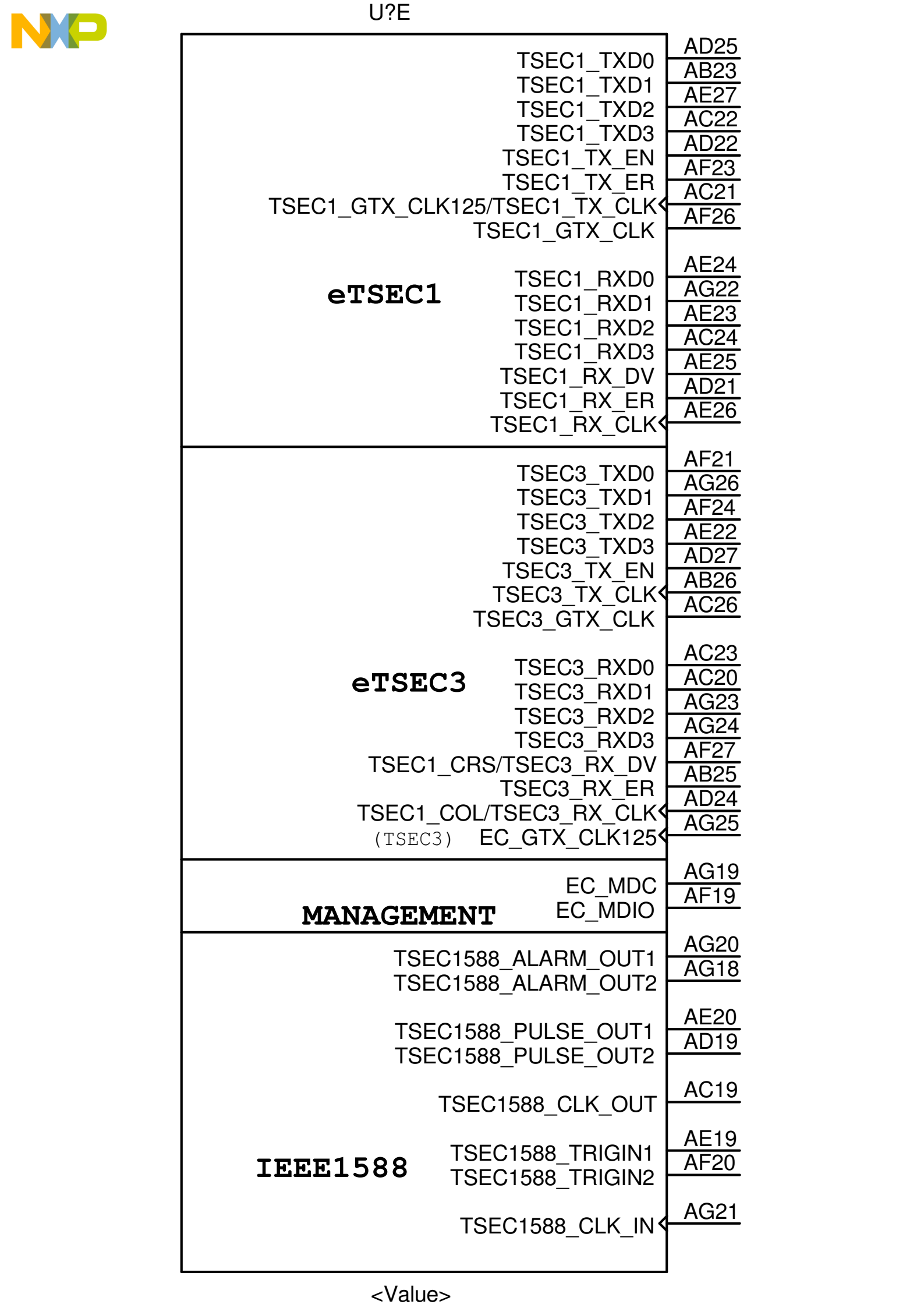
U?E
TSEC1_TXD0
TSEC1_TXD1
TSEC1_TXD2
TSEC1_TXD3
TSEC1_TX_EN
TSEC1_TX_ER
TSEC1_GTX_CLK125/TSEC1_TX_CLK
TSEC1_GTX_CLK
AD25
AB23
AE27
AC22
AD22
AF23
AC21
AF26
eTSEC1
TSEC1_RXD0
TSEC1_RXD1
TSEC1_RXD2
TSEC1_RXD3
TSEC1_RX_DV
TSEC1_RX_ER
TSEC1_RX_CLK
AE24
AG22
AE23
AC24
AE25
AD21
AE26
TSEC3_TXD0
TSEC3_TXD1
TSEC3_TXD2
TSEC3_TXD3
TSEC3_TX_EN
TSEC3_TX_CLK
TSEC3_GTX_CLK
AF21
AG26
AF24
AE22
AD27
AB26
AC26
eTSEC3
TSEC3_RXD0
TSEC3_RXD1
TSEC3_RXD2
TSEC3_RXD3
TSEC1_CRS/TSEC3_RX_DV
TSEC3_RX_ER
TSEC1_COL/TSEC3_RX_CLK
(TSEC3) EC_GTX_CLK125
AC23
AC20
AG23
AG24
AF27
AB25
AD24
AG25
MANAGEMENT
EC_MDC
EC_MDIO
AG19
AF19
IEEE1588
TSEC1588_ALARM_OUT1
TSEC1588_ALARM_OUT2
TSEC1588_PULSE_OUT1
TSEC1588_PULSE_OUT2
TSEC1588_CLK_OUT
TSEC1588_TRIGIN1
TSEC1588_TRIGIN2
TSEC1588_CLK_IN
AG20
AG18
AE20
AD19
AC19
AE19
AF20
AG21
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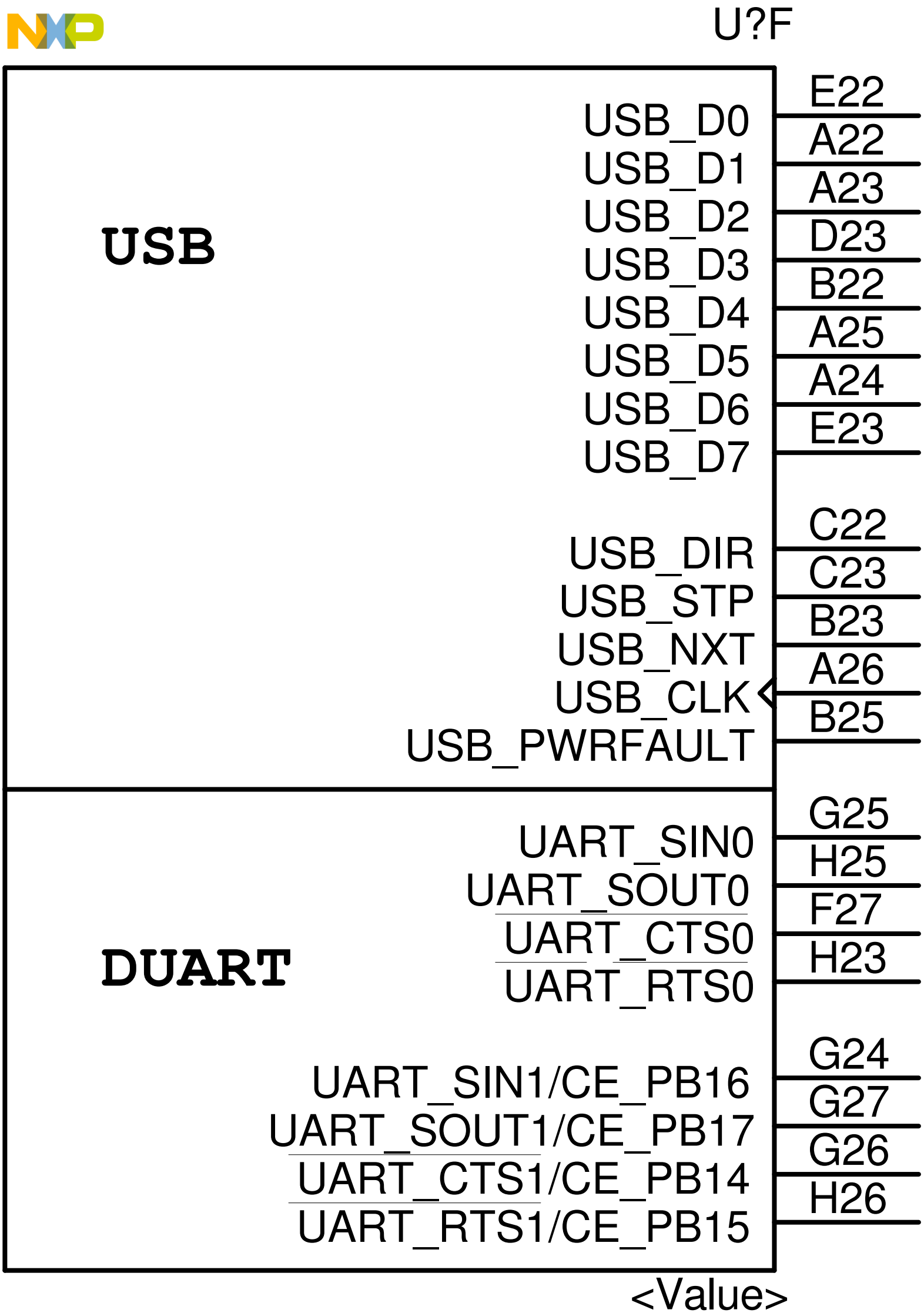
U?F
USB
USB_D0
USB_D1
USB_D2
USB_D3
USB_D4
USB_D5
USB_D6
USB_D7
USB_DIR
USB_STP
USB_NXT
USB_CLK
USB_PWRFAULT
E22
A22
A23
D23
B22
A25
A24
E23
C22
C23
B23
A26
B25
DUART
UART_SIN0
UART_SOUT0
UART_CTS0
UART_RTS0
UART_SIN1/CE_PB16
UART_SOUT1/CE_PB17
UART_CTS1/CE_PB14
UART_RTS1/CE_PB15
G25
H25
F27
H23
G24
G27
G26
H26
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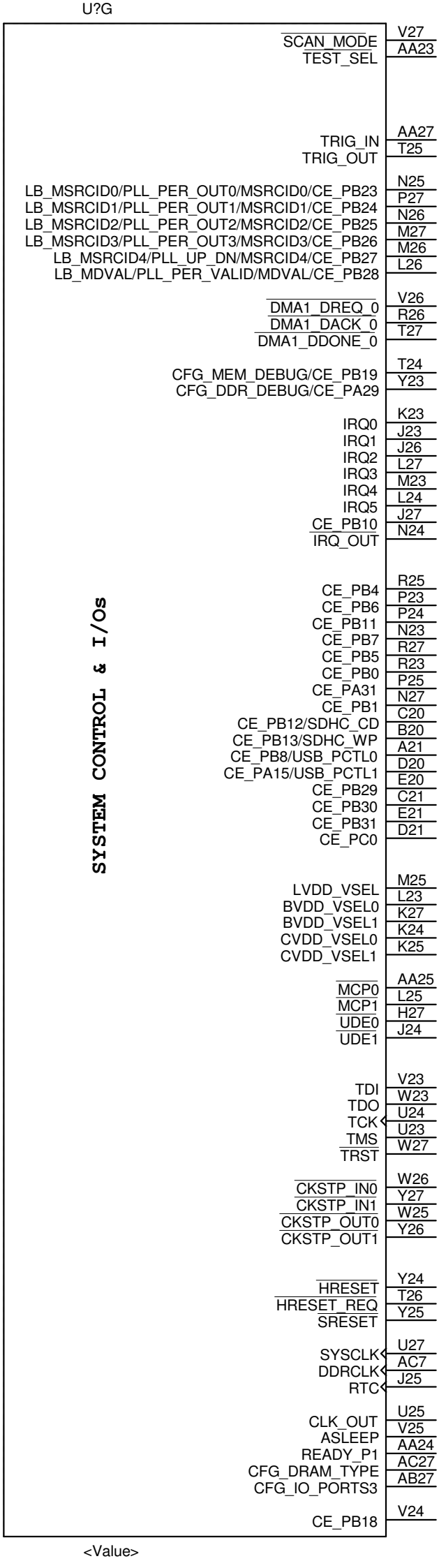
U?G
SCAN_MODE
TEST_SEL
V27
AA23
TRIG_IN
TRIG_OUT
AA27
T25
LB_MSRCID0/PLL_PER_OUT0/MSRCID0/CE_PB23
LB_MSRCID1/PLL_PER_OUT1/MSRCID1/CE_PB24
LB_MSRCID2/PLL_PER_OUT2/MSRCID2/CE_PB25
LB_MSRCID3/PLL_PER_OUT3/MSRCID3/CE_PB26
LB_MSRCID4/PLL_UP_DN/MSRCID4/CE_PB27
LB_MDVAL/PLL_PER_VALID/MDVAL/CE_PB28
N25
P27
N26
M27
M26
L26
DMA1_DREQ_0
DMA1_DACK_0
DMA1_DDONE_0
V26
R26
T27
CFG_MEM_DEBUG/CE_PB19
CFG_DDR_DEBUG/CE_PA29
T24
Y23
IRQ0
IRQ1
IRQ2
IRQ3
IRQ4
IRQ5
CE_PB10
IRQ_OUT
K23
J23
J26
L27
M23
L24
J27
N24
SYSTEM CONTROL & I/Os
CE_PB4
CE_PB6
CE_PB11
CE_PB7
CE_PB5
CE_PB0
CE_PA31
CE_PB1
CE_PB12/SDHC_CD
CE_PB13/SDHC_WP
CE_PB8/USB_PCTL0
CE_PA15/USB_PCTL1
CE_PB29
CE_PB30
CE_PB31
CE_PC0
R25
P23
P24
N23
R27
R23
P25
N27
C20
B20
A21
D20
E20
C21
E21
D21
LVDD_VSEL
BVDD_VSEL0
BVDD_VSEL1
CVDD_VSEL0
CVDD_VSEL1
M25
L23
K27
K24
K25
MCP0
MCP1
UDE0
UDE1
AA25
L25
H27
J24
TDI
TDO
TCK
TMS
TRST
V23
W23
U24
U23
W27
CKSTP_IN0
CKSTP_IN1
CKSTP_OUT0
CKSTP_OUT1
W26
Y27
W25
Y26
HRESET
HRESET_REQ
SRESET
Y24
T26
Y25
SYSCLK
DDRCLK
RTC
U27
AC7
J25
CLK_OUT
ASLEEP
READY_P1
CFG_DRAM_TYPE
CFG_IO_PORTS3
U25
V25
AA24
AC27
AB27
CE_PB18
V24
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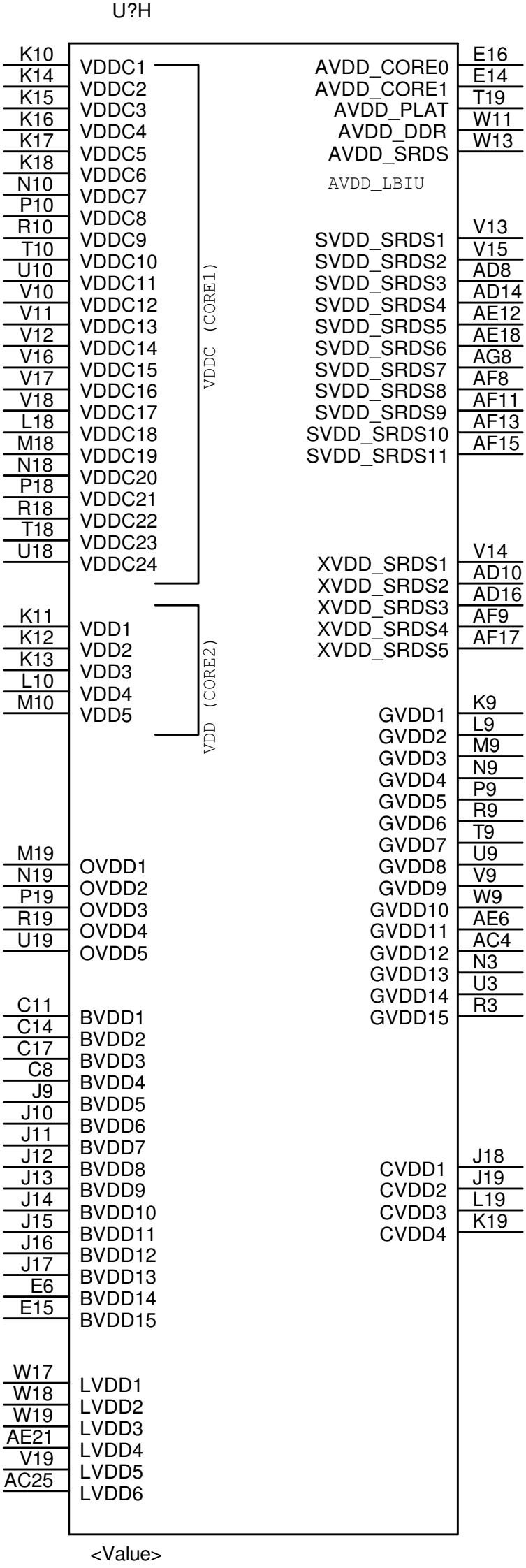
U?H
K10 VDDC1
K14 VDDC2
K15 VDDC3
K16 VDDC4
K17 VDDC5
K18 VDDC6
N10 VDDC7
P10 VDDC8
R10 VDDC9
T10 VDDC10
U10 VDDC11
V10 VDDC12
V11 VDDC13
V12 VDDC14
V16 VDDC15
V17 VDDC16
V18 VDDC17
L18 VDDC18
M18 VDDC19
N18 VDDC20
P18 VDDC21
R18 VDDC22
T18 VDDC23
U18 VDDC24
VDDC (CORE1)
K11 VDD1
K12 VDD2
K13 VDD3
L10 VDD4
M10 VDD5
VDD (CORE2)
M19 OVDD1
N19 OVDD2
P19 OVDD3
R19 OVDD4
U19 OVDD5
C11 BVDD1
C14 BVDD2
C17 BVDD3
C8 BVDD4
J9 BVDD5
J10 BVDD6
J11 BVDD7
J12 BVDD8
J13 BVDD9
J14 BVDD10
J15 BVDD11
J16 BVDD12
J17 BVDD13
E6 BVDD14
E15 BVDD15
W17 LVDD1
W18 LVDD2
W19 LVDD3
AE21 LVDD4
V19 LVDD5
AC25 LVDD6
AVDD_CORE0 E16
AVDD_CORE1 E14
AVDD_PLAT T19
AVDD_DDR W11
AVDD_SRDS W13
AVDD_LBIU
SVDD_SRDS1 V13
SVDD_SRDS2 V15
SVDD_SRDS3 AD8
SVDD_SRDS4 AD14
SVDD_SRDS5 AE12
SVDD_SRDS6 AE18
SVDD_SRDS7 AG8
SVDD_SRDS8 AF8
SVDD_SRDS9 AF11
SVDD_SRDS10 AF13
SVDD_SRDS11 AF15
XVDD_SRDS1 V14
XVDD_SRDS2 AD10
XVDD_SRDS3 AD16
XVDD_SRDS4 AF9
XVDD_SRDS5 AF17
GVDD1 K9
GVDD2 L9
GVDD3 M9
GVDD4 N9
GVDD5 P9
GVDD6 R9
GVDD7 T9
GVDD8 U9
GVDD9 V9
GVDD10 W9
GVDD11 AE6
GVDD12 AC4
GVDD13 N3
GVDD14 U3
GVDD15 R3
CVDD1 J18
CVDD2 J19
CVDD3 L19
CVDD4 K19
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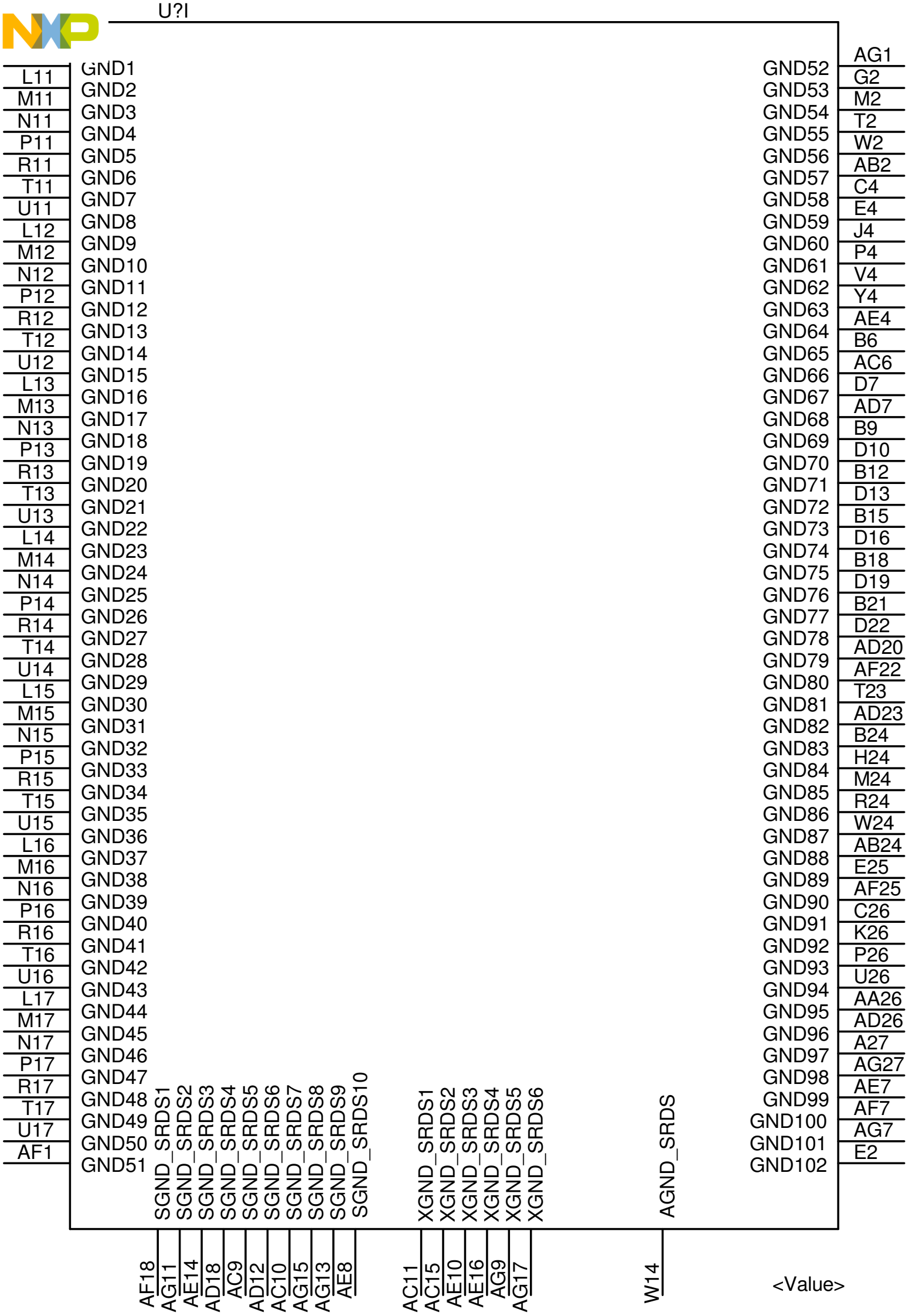
U?I
L11 GND1
M11 GND2
N11 GND3
P11 GND4
R11 GND5
T11 GND6
U11 GND7
L12 GND8
M12 GND9
N12 GND10
P12 GND11
R12 GND12
T12 GND13
U12 GND14
L13 GND15
M13 GND16
N13 GND17
P13 GND18
R13 GND19
T13 GND20
U13 GND21
L14 GND22
M14 GND23
N14 GND24
P14 GND25
R14 GND26
T14 GND27
U14 GND28
L15 GND29
M15 GND30
N15 GND31
P15 GND32
R15 GND33
T15 GND34
U15 GND35
L16 GND36
M16 GND37
N16 GND38
P16 GND39
R16 GND40
T16 GND41
U16 GND42
L17 GND43
M17 GND44
N17 GND45
P17 GND46
R17 GND47
T17 GND48
U17 GND49
AF1 GND50
GND51
AG1 GND52
G2 GND53
M2 GND54
T2 GND55
W2 GND56
AB2 GND57
C4 GND58
E4 GND59
J4 GND60
P4 GND61
V4 GND62
Y4 GND63
AE4 GND64
B6 GND65
AC6 GND66
D7 GND67
AD7 GND68
B9 GND69
D10 GND70
B12 GND71
D13 GND72
B15 GND73
D16 GND74
B18 GND75
D19 GND76
B21 GND77
D22 GND78
AD20 GND79
AF22 GND80
T23 GND81
AD23 GND82
B24 GND83
H24 GND84
M24 GND85
R24 GND86
W24 GND87
AB24 GND88
E25 GND89
AF25 GND90
C26 GND91
K26 GND92
P26 GND93
U26 GND94
AA26 GND95
AD26 GND96
A27 GND97
AG27 GND98
AE7 GND99
AF7 GND100
AG7 GND101
E2 GND102
SGND_SRDS1
SGND_SRDS2
SGND_SRDS3
SGND_SRDS4
SGND_SRDS5
SGND_SRDS6
SGND_SRDS7
SGND_SRDS8
SGND_SRDS9
SGND_SRDS10
XGND_SRDS1
XGND_SRDS2
XGND_SRDS3
XGND_SRDS4
XGND_SRDS5
XGND_SRDS6
AGND_SRDS
AF18
AG11
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AG17
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