



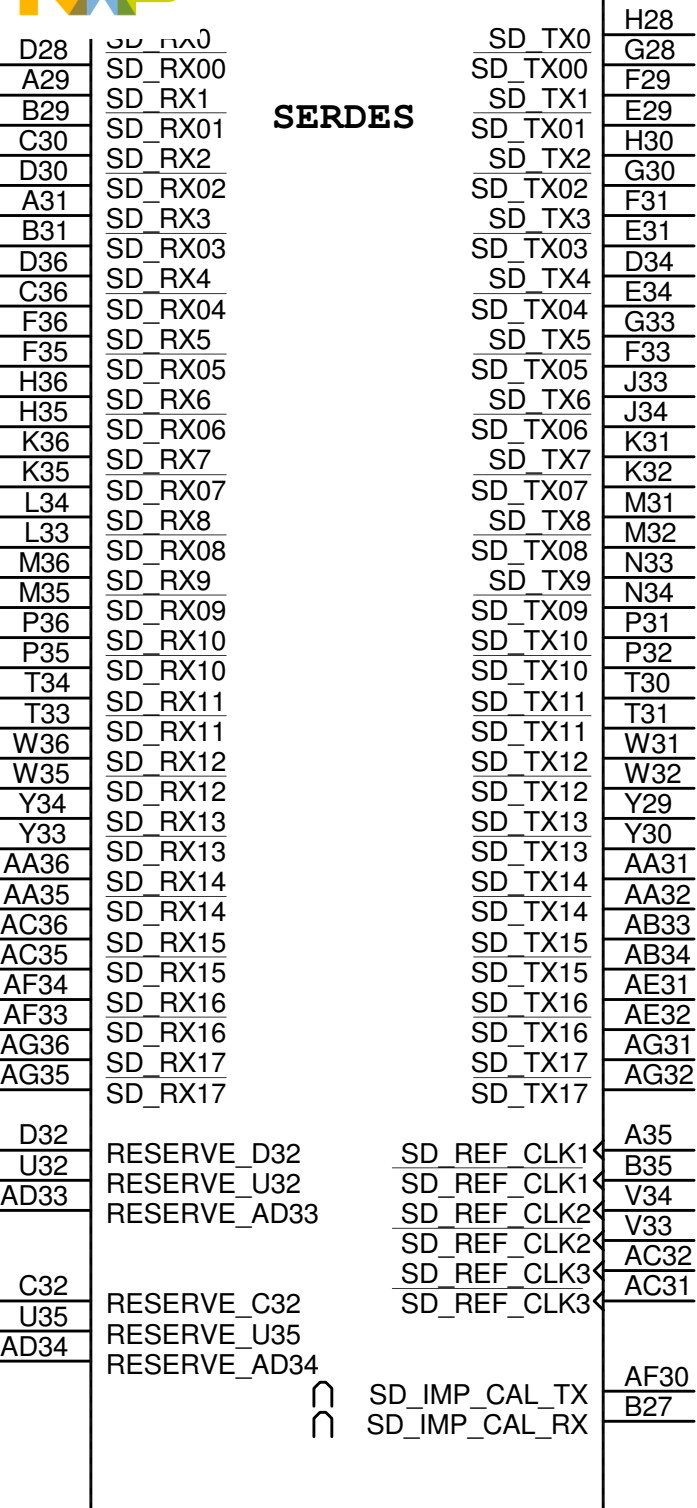
for P3041

DDR2
P5020

N3		RESERVE_C13	C13
M5	RESERVE_K3	RESERVE_A12	A12
N5	RESERVE_M5	RESERVE_B9	B9
J4	RESERVE_N5	RESERVE_A8	A8
J2	RESERVE_J4	RESERVE_A13	A13
L3	RESERVE_J2	RESERVE_B13	B13
L4	RESERVE_L3	RESERVE_B10	B10
	RESERVE_L4	RESERVE_A9	A9
N2		RESERVE_A7	A7
Y1	RESERVE_N2	RESERVE_D6	D6
	RESERVE_Y1	RESERVE_A4	A4
B12		RESERVE_B4	B4
B6	RESERVE_B12	RESERVE_C7	C7
C4	RESERVE_B6	RESERVE_B7	B7
G3	RESERVE_C4	RESERVE_C5	C5
AG1	RESERVE_G3	RESERVE_D5	D5
AL3	RESERVE_AG1	RESERVE_B1	B1
AP2	RESERVE_AL3	RESERVE_B3	B3
AP6	RESERVE_AP2	RESERVE_D3	D3
K2	RESERVE_AP6	RESERVE_E1	E1
	RESERVE_K2	RESERVE_A3	A3
A10		RESERVE_A2	A2
A5	RESERVE_A10	RESERVE_D1	D1
C2	RESERVE_A5	RESERVE_D2	D2
G6	RESERVE_C2	RESERVE_F4	F4
AH2	RESERVE_G6	RESERVE_F5	F5
AM4	RESERVE_AH2	RESERVE_H4	H4
AR1	RESERVE_AM4	RESERVE_H6	H6
AR6	RESERVE_AR1	RESERVE_E4	E4
L1	RESERVE_AR6	RESERVE_E3	E3
	RESERVE_L1	RESERVE_H3	H3
A11		RESERVE_H1	H1
A6	RESERVE_A11	RESERVE_AG4	AG4
C1	RESERVE_A6	RESERVE_AG2	AG2
G5	RESERVE_C1	RESERVE_AJ3	AJ3
AH3	RESERVE_G5	RESERVE_AJ1	AJ1
AM3	RESERVE_AH3	RESERVE_AF4	AF4
AP1	RESERVE_AM3	RESERVE_AF3	AF3
AT6	RESERVE_AP1	RESERVE_AH1	AH1
K1	RESERVE_AT6	RESERVE_AJ4	AJ4
	RESERVE_K1	RESERVE_AL6	AL6
AA3		RESERVE_AL5	AL5
AA1	RESERVE_AA3	RESERVE_AN4	AN4
M1	RESERVE_AA1	RESERVE_AN5	AN5
	RESERVE_M1	RESERVE_AK5	AK5
Y4		RESERVE_AK4	AK4
U1	RESERVE_Y4	RESERVE_AM6	AM6
U4	RESERVE_U1	RESERVE_AM7	AM7
T1	RESERVE_U4	RESERVE_AN1	AN1
T2	RESERVE_T1	RESERVE_AP3	AP3
T3	RESERVE_T2	RESERVE_AT1	AT1
R1	RESERVE_T3	RESERVE_AT2	AT2
R4	RESERVE_R1	RESERVE_AM1	AM1
R2	RESERVE_R4	RESERVE_AN2	AN2
P1	RESERVE_R2	RESERVE_AR3	AR3
AA2	RESERVE_P1	RESERVE_AT3	AT3
P3	RESERVE_AA2	RESERVE_AP5	AP5
N1	RESERVE_P3	RESERVE_AT5	AT5
AC4	RESERVE_N1	RESERVE_AP8	AP8
N3	RESERVE_AC4	RESERVE_AT8	AT8
M2	RESERVE_N3	RESERVE_AR4	AR4
	RESERVE_M2	RESERVE_AT4	AT4
AC1		RESERVE_AR7	AR7
AE1	RESERVE_AC1	RESERVE_AT7	AT7
AB3	RESERVE_AE1	RESERVE_AD2	AD2
AE2	RESERVE_AB3	RESERVE_AF1	AF1
	RESERVE_AE2	RESERVE_AD1	AD1
R5		RESERVE_AE3	AE3
T5	RESERVE_R5	RESERVE_AA4	AA4
P4	RESERVE_T5	RESERVE_Y6	Y6
M4	RESERVE_P4		
	RESERVE_M4		
W3			AC3
W4	RESERVE_W3	RESERVE_AC3	AB1
V3	RESERVE_W4	RESERVE_AB1	AB2
V4	RESERVE_V3	RESERVE_AB2	
V1	RESERVE_V4		
V2	RESERVE_V1		
W2	RESERVE_V2		
W1	RESERVE_W2		
	RESERVE_W1		



U?B



SERDES

<Value>

ETH. CNTR1

EC1_GTX_CLK125	AK34
	AT35
EC1_TXD0	AR34
EC1_TXD1	AT34
EC1_TXD2	AP36
EC1_TXD3	AR36
EC1_TX_EN	AP35
EC1_GTX_CLK	
	AN36
EC1_RXD0	AN35
EC1_RXD1	AN34
EC1_RXD2	AM33
EC1_RXD3	AM34
EC1_RX_DV	AM36
EC1_RX_CLK	

ETH. CNTR2

RESERVE_AH29	AH29
(RSRV for P3041/NC FOR P4080)	
EC2_GTX_CLK125	AL33
	AT30
EC2_TXD0	AR30
EC2_TXD1	AP30
EC2_TXD2	AT31
EC2_TXD3	AR31
EC2_TX_EN	AN31
EC2_GTX_CLK	
	AT32
EC2_RXD0	AP32
EC2_RXD1	AN32
EC2_RXD2	AP33
EC2_RXD3	AR33
EC2_RX_DV	AT33
EC2_RX_CLK	



D

LOCAL BUS

K20
G19
H19
J19
G18
J24
K23
H17
H16

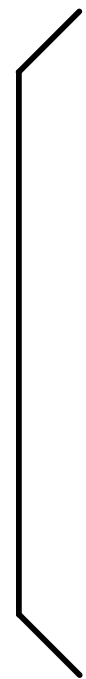
LA27
LA28
LA29
LA30
LA31
LDP0
LDP1
LDP2
LDP3

P4080 ADDRESS ONLY

LAD0	K26
LAD1	L26
LAD2	J26
LAD3	H25
LAD4	F25
LAD5	H24
LAD6	G24
LAD7	G23
LAD8	E23
LAD9	D23
LAD10	J22
LAD11	G22
LAD12	F19
LAD13	J18
LAD14	K18
LAD15	J17
LAD16	J25
LAD17	G25
LAD18	H23
LAD19	F22
LAD20	H22
LAD21	E21
LAD22	F21
LAD23	H21
LAD24	K21
LAD25	G20
LAD26	J20
LAD27	D26
LAD28	E18
LAD29	F18
LAD30	J15
LAD31	F17
$\overline{\text{LCS0}}$	D19
$\overline{\text{LCS1}}$	D20
$\overline{\text{LCS2}}$	E20
$\overline{\text{LCS3}}$	D21
$\overline{\text{LCS4}}$	D22
$\overline{\text{LCS5}}$	B23
$\overline{\text{LCS6}}$	F24
$\overline{\text{LCS7}}$	G26
$\overline{\text{LWE0}}$	D24
$\overline{\text{LWE1}}$	A24
$\overline{\text{LWE2}}$	J16
$\overline{\text{LWE3}}$	K15
LBCTL	C22
LALE	A23
LGPL0/LFCLE	B25
LGPL1/LFALE	E25
LGPL2/LOE/LFRE	D25
LGPL3/LFWP	H26
LGPL4/LGTA/LUPWAIT/LPBSE	C25
LGPL5	E26
LCLK0	C24
LCLK1	C23

<Value>

I2C



IIC1_SCL
IIC1_SDA

AH15
AN14



IIC2_SCL
IIC2_SDA

AM15
AL14



IIC4_SCL/EVT5
IIC4_SDA/EVT6

AG14
AL15

ETH MII2



EMI2_MDC
EMI2_MDIO

AK30
AJ30

ETH MII1

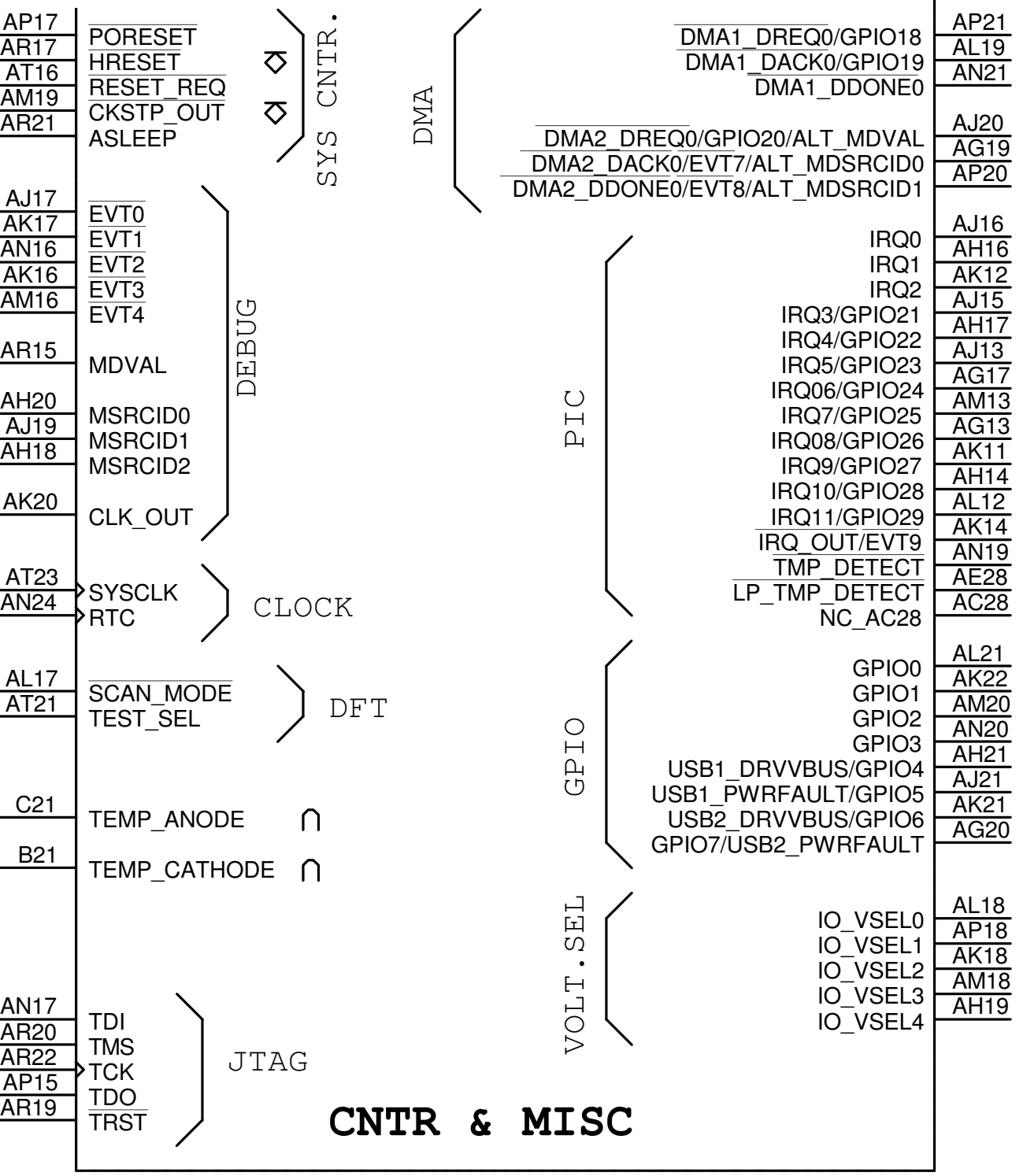


EMI1_MDC
EMI1_MDIO

AJ33
AL32



U?F



- AP21**
- AL19**
- AN21**
- AJ20**
- AG19**
- AP20**
- AJ16**
- AH16**
- AK12**
- AJ15**
- AH17**
- AJ13**
- AG17**
- AM13**
- AG13**
- AK11**
- AH14**
- AL12**
- AK14**
- AN19**
- AE28**
- AC28**
- AL21**
- AK22**
- AM20**
- AN20**
- AH21**
- AJ21**
- AK21**
- AG20**
- AL18**
- AP18**
- AK18**
- AM18**
- AH19**

<Value>



C12	GVDD4
C6	GVDD5
D4	GVDD6
D10	GVDD7
D16	GVDD8
E14	GVDD9
E8	GVDD10
E2	GVDD11
F6	GVDD12
F12	GVDD13
AR8	GVDD14
G4	GVDD15
G10	GVDD16
G16	GVDD17
H14	GVDD18
H8	GVDD19
H2	GVDD20
J6	GVDD21
K10	GVDD22
K4	GVDD23
L2	GVDD24
L8	GVDD25
M6	GVDD26
N4	GVDD27
N10	GVDD28
P8	GVDD29
P2	GVDD30
R6	GVDD31
T10	GVDD32
T4	GVDD33
J12	GVDD34
U2	GVDD35
U8	GVDD36
V7	GVDD37
AK10	GVDD38
W10	GVDD39
AA6	GVDD40
AR2	GVDD41
Y2	GVDD42
Y8	GVDD43
AC2	GVDD44
AD6	GVDD45
AE10	GVDD46
AE4	GVDD47
AF2	GVDD48
AF8	GVDD49
AB4	GVDD50
AB10	GVDD51
AC8	GVDD52
AG6	GVDD53
AH10	GVDD54
AH4	GVDD55
AJ2	GVDD56
AJ8	GVDD57
AR14	GVDD58
AK6	GVDD59
AL4	GVDD60
AL10	GVDD61
AM2	GVDD62
AM8	GVDD63
AP10	GVDD64
AN12	GVDD65
AN6	GVDD66
AP4	GVDD67
	GVDD68
B24	BVDD1
K22	BVDD2
F20	BVDD3
F26	BVDD4
E24	BVDD5
E22	BVDD6
K24	BVDD7
H20	BVDD8
H18	BVDD9
AK33	LVDD1
AP31	LVDD2
AL31	LVDD3
AN33	LVDD4
AJ35	LVDD5
AR35	LVDD6
AM35	LVDD7
AT17	POVDD

PS

DDR

LB

ETH

GENERAL I/O

SPI, SDHC

SERDES CORE

SERDES TRANSCEIVER

OVDD1	AN22
OVDD2	AJ14
OVDD3	AJ18
OVDD4	AL16
OVDD5	AJ12
OVDD6	AN18
OVDD7	AG21
OVDD8	AL20
OVDD9	AT15
OVDD10	AJ23
OVDD11	AP16
OVDD12	AR24
CVDD1	AG24
CVDD2	AP29
CVDD3	AJ29
SVDD1	A30
SVDD2	A34
SVDD3	AA33
SVDD4	AB35
SVDD5	AD36
SVDD6	AE33
SVDD7	AF35
SVDD8	AG34
SVDD9	B28
SVDD10	B32
SVDD11	B36
SVDD12	C31
SVDD13	C34
SVDD14	C35
SVDD15	D29
SVDD16	E36
SVDD17	F34
SVDD18	G35
SVDD19	J36
SVDD20	K34
SVDD21	L35
SVDD22	M33
SVDD23	N36
SVDD24	R34
SVDD25	R35
SVDD26	U33
SVDD27	V35
SVDD28	W34
SVDD29	Y36
SVDD30	AH36
XVDD1	AA29
XVDD2	AB30
XVDD3	AB31
XVDD4	AC33
XVDD5	AD32
XVDD6	AE30
XVDD7	AF31
XVDD8	E32
XVDD9	E33
XVDD10	F28
XVDD11	F30
XVDD12	G32
XVDD13	H31
XVDD14	H33
XVDD15	J28
XVDD16	J30
XVDD17	J32
XVDD18	K30
XVDD19	L30
XVDD20	L31
XVDD21	M29
XVDD22	N31
XVDD23	P30
XVDD24	P33
XVDD25	R29
XVDD26	R31
XVDD27	T29
XVDD28	T32
XVDD29	U30
XVDD30	V30
XVDD31	V32
XVDD32	W29
XVDD33	Y31
XVDD34	AH32

P14	VDD_CA_PL4		
N19	VDD_CA_PL5		
M20	VDD_CA_PL6		
N21	VDD_CA_PL7		
M16	VDD_CA_PL8		
N15	VDD_CA_PL9		
P16	VDD_CA_PL10		
T16	VDD_CA_PL11		
R17	VDD_CA_PL12		
T18	VDD_CA_PL13		
R15	VDD_CA_PL14		
N17	VDD_CA_PL15		
M18	VDD_CA_PL16		
P18	VDD_CA_PL17		
	VDD_CA_PL18		
K16	SENSEVDD_CA_PL		
W15	VDD_CB1		
W19	VDD_CB2		
AA19	VDD_CB3		
Y20	VDD_CB4		
AB14	VDD_CB5		
AA21	VDD_CB6		
Y16	VDD_CB7		
AA15	VDD_CB8		
AC15	VDD_CB9		
AA17	VDD_CB10		
AC17	VDD_CB11		
W17	VDD_CB12		
Y18	VDD_CB13		
AB18	VDD_CB14		
AB16	VDD_CB15		
AC19	VDD_CB16		
AB20	VDD_CB17		
AG15	SENSEVDD_CB		
AD28	VDD_LP		
A33	AVDD_SRDS1	PLL SERDES	
U36	AVDD_SRDS2		
AE35	AVDD_SRDS3		
A20	AVDD_CC1	PLL CORE GROUP A	
AT18	AVDD_CC2	PLL CORE GROUP B	
AT20	AVDD_PLAT	PLL PLAT	
A19	AVDD_DDR	PLL DDR	
AH25	USB1_VDD_1P0	PLL USB	
AH26	USB2_VDD_1P0		
AJ25	USB1_VDD_3P3_1	USB TRANSCIEIVER	
AL24	USB1_VDD_3P3_2		
AJ26	USB2_VDD_3P3_1		
AJ27	USB2_VDD_3P3_2		
M14	VDD_CA_PL19		
U17	VDD_CA_PL20		
U19	VDD_CA_PL21		
T14	VDD_CA_PL22		
AD14	VDD_CA_PL23		
AD16	VDD_CA_PL24		
AD18	VDD_CA_PL25		
AD20	VDD_CA_PL26		
Y14	VDD_CA_PL27		

M26
P26
T26
V26
Y26
AB26
AD26
N11
R11
W11
AA11
AE11
M12
P12
T12
V12
Y12
AB12
AD12
AE13
AE15
V16
AE17
L11
AE19
U11
AC11
V20
AE21
V22
U13
R27
U23
W23
AA27
AC27
AE23
M24
P24
T24
V24
Y24
AB24
AD24
N25
R25
U25
W25
AA25
AC25
N27
U27
W28
AE25
AF24
AF22
AF20
AF16
W13
AF18
V14
V18
L13
L15
L17
L19
L21
L23
L25
AF14
N23
R23
AA23
AC23
U21
W21
U15
AC21
AD22
M22
N13
AC13
P22
T22
Y22
AB22
AA13
R13
AF11
L27

RESERVE_AF11
RESERVE_L27



U1	SGND1
FE1	SGND2
FE2	SGND3
FE3	SGND4
FE4	SGND5
FE5	SGND6
FE6	SGND7
FE7	SGND8
FE8	SGND9
FE9	SGND10
FE10	SGND11
FE11	SGND12
FE12	SGND13
FE13	SGND14
FE14	SGND15
FE15	SGND16
FE16	SGND17
FE17	SGND18
FE18	SGND19
FE19	SGND20
FE20	SGND21
FE21	SGND22
FE22	SGND23
FE23	SGND24
FE24	SGND25
FE25	SGND26
FE26	SGND27
FE27	SGND28
FE28	SGND29
FE29	SGND30
FE30	SGND31
FE31	SGND32

GND

D33	XGND1
E28	XGND2
E30	XGND3
F32	XGND4
G29	XGND5
G31	XGND6
H23	XGND7
H32	XGND8
H34	XGND9
J29	XGND10
J31	XGND11
K28	XGND12
K29	XGND13
L23	XGND14
L32	XGND15
M30	XGND16
N29	XGND17
N30	XGND18
N32	XGND19
P29	XGND20
P34	XGND21
R30	XGND22
R32	XGND23
U29	XGND24
U31	XGND25
V29	XGND26
V31	XGND27
W30	XGND28
Y32	XGND29
AA30	XGND30
AB32	XGND31
AC30	XGND32
AC34	XGND33
AD30	XGND34
AD31	XGND35
AF32	XGND36
AG30	XGND37
AH31	XGND38

K17	SENSEGND_CA_FL
AG16	SENSEGND_CB

AF12	RESERVE_AF12
K27	RESERVE_K27

B33	AGND_SRD61
T36	AGND_SRD62
AE36	AGND_SRD63

GND58	AD3
GND59	AD9
GND60	AE7
GND61	AF5
GND62	AG3
GND63	AH7
GND64	AJ5
GND65	AK3
GND66	AN3
GND67	AM5
GND68	AL7
GND69	AK9
GND70	AJ11
GND71	AH13
GND72	AH5
GND73	AP7
GND74	AN9
GND75	AM11
GND76	AL13
GND77	AK15
GND78	AG18
GND79	AR11
GND80	AP13
GND81	AN15
GND82	AM17
GND83	AK19
GND84	AP13
GND85	AR18
GND86	AE27
GND87	AP19
GND88	AH22
GND89	AM23
GND90	AL23
GND91	AR16
GND92	AT22
GND93	AP23
GND94	AR32
GND95	AK28
GND96	AE27
GND97	L16
GND98	AP34
GND99	AJ32
GND100	AN30
GND101	AH34
GND102	AT36
GND103	AL34
GND104	AM32
GND105	AE26
GND106	AC26
GND107	AA26
GND108	W26
GND109	U26
GND110	R26
GND111	N26
GND112	M11
GND113	P11
GND114	T11
GND115	Y11
GND116	Y11
GND117	AB11
GND118	AD11
GND119	AE12
GND120	AC12
GND121	AA12
GND122	W12
GND123	U12
GND124	R12
GND125	N12
GND126	M13
GND127	P13
GND128	T13
GND129	Y13
GND130	Y13
GND131	AB13
GND132	AD13
GND133	AE14
GND134	AC14
GND135	AA14
GND136	W14
GND137	U14
GND138	R14
GND139	N14
GND140	L14
GND141	M15
GND142	P15
GND143	T15
GND144	Y15
GND145	Y15
GND146	AB15
GND147	AD15
GND148	AE15
GND149	W16
GND150	AC16
GND151	AA16
GND152	AE16
GND153	U16
GND154	R16
GND155	N16
GND156	M17
GND157	P17
GND158	T17
GND159	N18
GND160	R18
GND161	U18
GND162	Y17
GND163	AB17
GND164	AD17
GND165	AE17
GND166	W18
GND167	AC18
GND168	AA18
GND169	AS18
GND170	AF19
GND171	AD19
GND172	AB19
GND173	Y18
GND174	V19
GND175	T19
GND176	P19
GND177	M19
GND178	N20
GND179	R20
GND180	U20
GND181	AE20
GND182	AA20
GND183	AC20
GND184	W20
GND185	AF21
GND186	AD21
GND187	AB21

G17	GND231
AG17	GND230
AE17	GND229
AD17	GND228
Y17	GND227
W17	GND226
V17	GND225
U17	GND224
T17	GND223
S17	GND222
R17	GND221
Q17	GND220
P17	GND219
O17	GND218
N17	GND217
M17	GND216
L17	GND215
K17	GND214
J17	GND213
I17	GND212
H17	GND211
G17	GND210
F17	GND209
E17	GND208
D17	GND207
C17	GND206
B17	GND205
A17	GND204
Z17	GND203
Y17	GND202
X17	GND201
W17	GND200
V17	GND199
U17	GND198
T17	GND197
S17	GND196
R17	GND195
Q17	GND194
P17	GND193
O17	GND192
N17	GND191
M17	GND190
L17	GND189
K17	GND188
J17	GND187
I17	GND186
H17	GND185
G17	GND184
F17	GND183
E17	GND182
D17	GND181
C17	GND180
B17	GND179
A17	GND178
Z17	GND177
Y17	GND176
X17	GND175
W17	GND174
V17	GND173
U17	GND172
T17	GND171
S17	GND170
R17	GND169
Q17	GND168
P17	GND167
O17	GND166
N17	GND165
M17	GND164
L17	GND163
K17	GND162
J17	GND161
I17	GND160
H17	GND159
G17	GND158
F17	GND157
E17	GND156
D17	GND155
C17	GND154
B17	GND153
A17	GND152
Z17	GND151
Y17	GND150
X17	GND149
W17	GND148
V17	GND147
U17	GND146
T17	GND145
S17	GND144
R17	GND143
Q17	GND142
P17	GND141
O17	GND140
N17	GND139
M17	GND138
L17	GND137
K17	GND136
J17	GND135
I17	GND134
H17	GND133
G17	GND132
F17	GND131
E17	GND130
D17	GND129
C17	GND128
B17	GND127
A17	GND126
Z17	GND125
Y17	GND124
X17	GND123
W17	GND122
V17	GND121
U17	GND120
T17	GND119
S17	GND118
R17	GND117
Q17	GND116
P17	GND115
O17	GND114
N17	GND113
M17	GND112
L17	GND111
K17	GND110
J17	GND109
I17	GND108
H17	GND107
G17	GND106
F17	GND105
E17	GND104
D17	GND103
C17	GND102
B17	GND101
A17	GND100
Z17	GND99
Y17	GND98
X17	GND97
W17	GND96
V17	GND95
U17	GND94
T17	GND93
S17	GND92
R17	GND91
Q17	GND90
P17	GND89
O17	GND88
N17	GND87
M17	GND86
L17	GND85
K17	GND84
J17	GND83
I17	GND82
H17	GND81
G17	GND80
F17	GND79
E17	GND78
D17	GND77
C17	GND76
B17	GND75
A17	GND74
Z17	GND73
Y17	GND72
X17	GND71
W17	GND70
V17	GND69
U17	GND68
T17	GND67
S17	GND66
R17	GND65
Q17	GND64
P17	GND63
O17	GND62
N17	GND61
M17	GND60
L17	GND59
K17	GND58
J17	GND57
I17	GND56
H17	GND55
G17	GND54
F17	GND53
E17	GND52
D17	GND51
C17	GND50
B17	GND49
A17	GND48
Z17	GND47
Y17	GND46
X17	GND45
W17	GND44
V17	GND43
U17	GND42
T17	GND41
S17	GND40
R17	GND39
Q17	GND38
P17	GND37
O17	GND36
N17	GND35
M17	GND34
L17	GND33
K17	GND32
J17	GND31
I17	GND30
H17	GND29
G17	GND28
F17	GND27
E17	GND26
D17	GND25
C17	GND24
B17	GND23
A17	GND22
Z17	GND21
Y17	GND20
X17	GND19
W17	GND18
V17	GND17
U17	GND16
T17	GND15
S17	GND14
R17	GND13
Q17	GND12
P17	GND11
O17	GND10
N17	GND9
M17	GND8
L17	GND7
K17	GND6
J17	GND5
I17	GND4
H17	GND3
G17	GND2
F17	GND1

<Value>



NC/RESERVED

B26	NC_A27	RESERVE_A25	A25
C19	NC_B26	RESERVE_F1	F1
C20	NC_C19	RESERVE_F2	F2
C26	NC_C20	RESERVE_G1	G1
C27	NC_C26	RESERVE_G2	G2
D18	NC_C27	RESERVE_L28	L28
D27	NC_D18	RESERVE_M28	M28
E16	NC_D27	RESERVE_N28	N28
E27	NC_E16	RESERVE_P28	P28
G27	NC_E27	RESERVE_AG11	AG11
H12	NC_G27	RESERVE_AG12	AG12
H13	NC_H12	RESERVE_AG25	AG25
H15	NC_H13	RESERVE_AH11	AH11
H27	NC_H15	RESERVE_AH12	AH12
J11	NC_H27		
J13	NC_J11		
J14	NC_J13		
K11	NC_J14	RESERVE_AK1	AK1
K12	NC_K11	RESERVE_AK2	AK2
K13	NC_K12	RESERVE_AL1	AL1
K14	NC_K13	RESERVE_AL2	AL2
R28	NC_K14		
T28	NC_R28		
U28	NC_T28		
V28	NC_U28		
W27	NC_V28		
Y28	NC_W27_DET		
AA28	NC_Y28		
AB28	NC_AA28	RESERVE_A21	A21
AB29	NC_AB28		
AC29	NC_AB29	RESERVE_AT19	AT19
AD29	NC_AC29		
AE29	NC_AD29		
AF26	NC_AE29		
AF27	NC_AF26		
AF28	NC_AF27		
AF29	NC_AF28		
AG26	NC_AF29		
AG27	NC_AG26		
AG28	NC_AG27		
AG29	NC_AG28		
AH30	NC_AG29		
AM12	NC_AH30		
AN13	NC_AM12		
AP11	NC_AN13		
AP14	NC_AP11		
AT14	NC_AP14		
	NC_AT14		

PACKAGE_NC (A1) ○



DDR1

LIU
M10
J8
J7
L7
L9
N8
Y7
A16
D14
D11
G11
AD7
AH8
AL11
AT10
K8
C16
G14
D9
G9
AD5
AH6
AM10
AT12
K6
B16
F14
D8
G8
AD4
AH5
AM9
AT11
K5
AA8
Y10
M8
Y9
U6
U7
U9
U10
T8
T9
R8
R7
P6
AA7
P7
N6
AE8
M7
L6
AC9
AE9
AB9
AF9
P10
R10
P9
N9
W6
W5
V6
V5
V8
V9
W9
W8

MECC2
MECC3
MECC4
MECC5
MECC6
MECC7
MAPAR_ERR
MAPAR_OUT
MDM0
MDM1
MDM2
MDM3
MDM4
MDM5
MDM6
MDM7
MDM8
MDQS0
MDQS1
MDQS2
MDQS3
MDQS4
MDQS5
MDQS6
MDQS7
MDQS8
MDQS0
MDQS1
MDQS2
MDQS3
MDQS4
MDQS5
MDQS6
MDQS7
MDQS8
MBA0
MBA1
MBA2
MA00
MA01
MA02
MA03
MA04
MA05
MA06
MA07
MA08
MA09
MA10
MA11
MA12
MA13
MA14
MA15
MCS0
MCS1
MCS2
MCS3
MCKE0
MCKE1
MCKE2
MCKE3
MCK0
MCK0
MCK1
MCK1
MCK2
MCK2
MCK3
MCK3

MDQ00
MDQ01
MDQ02
MDQ03
MDQ04
MDQ05
MDQ06
MDQ07
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MDQ51
MDQ52
MDQ53
MDQ54
MDQ55
MDQ56
MDQ57
MDQ58
MDQ59
MDQ60
MDQ61
MDQ62
MDQ63
MODT0
MODT1
MODT2
MODT3
MDIC0
MDIC1
MCAS
MRAS
MWE
MVREF

A17
D17
C14
A14
C17
B17
A15
B15
D15
G15
E12
G12
F16
E15
E13
F13
C8
D12
E9
E10
C11
C10
E6
E7
F7
F11
H10
J10
F10
F8
H7
H9
AC7
AC6
AF6
AF7
AB5
AB6
AE5
AE6
AG5
AH9
AJ9
AJ10
AG8
AG7
AJ6
AJ7
AL9
AL8
AN10
AN11
AK8
AK7
AN7
AN8
AT9
AR10
AT13
AR13
AP9
AR9
AR12
AP12
AD10
AG10
AD8
AF10
T6
AA5
AC10
AA10
AB8
B19

General for
DDR1&DDR2



UART

UART2_SOUT/GPIO9	AJ22
UART2_RTS/UART4_SOUT/GPIO13	AK23
UART2_SIN/GPIO11	AN23
UART2_CTS/UART4_SIN/GPIO15	AH23
UART1_SOUT/GPIO8	AL22
UART1_SIN/GPIO10	AR23
UART1_RTS/UART3_SOUT/GPIO12	AM22
UART1_CTS/UART3_SIN/GPIO14	AP22

SPI / SDHC

SPI_MISO	AH28
SPI_MOSI	AT29
SPI_CLK	AK29
SPI_CS0/SDHC_DAT4	AN29
SPI_CS1/SDHC_DAT5	AJ28
SPI_CS2/SDHC_DAT6	AR29
SPI_CS3/SDHC_DAT7	AM29

SDHC

SDHC_DAT0	AP24
SDHC_DAT1	AT24
SDHC_DAT2	AM23
SDHC_DAT3	AG22
SDHC_CLK	AL23
SDHC_CMD	AG23

SDHC / I2C

IIC3_SCL/SDHC_CD/GPIO16	AK13
IIC3_SDA/SDHC_WP/GPIO17	AM14



AL35
 AM30
 AL30
 AJ34
 AJ36
 AL36
 AK36
 AK35
 AK32
 AK31
 AM31
 AJ31

TSEC_1588_CLK_IN
 TSEC_1588_CLK_OUT
 TSEC_1588_PULSE_OUT1
 TSEC_1588_PULSE_OUT2/EC1_CRS/GPIO31
 TSEC_1588_ALARM_OUT1
 TSEC_1588_TRIG_IN1
 TSEC_1588_TRIG_IN2/EC1_RX_ER
 TSEC_1588_ALARM_OUT2/EC1_COL/GPIO30
 EC_XTRNL_RX_STMP1
 EC_XTRNL_RX_STMP2
 EC_XTRNL_TX_STMP1
 EC_XTRNL_TX_STMP2

**ETH EXT
 TIMESTAMP**

IEEE 1588

USB PHY

USB_CLKIN ← AM24
 USB1_UDP ← AT27
 USB1_UDM ← AT26
 USB1_VBUS_CLMP ← AK25
 USB1_UID ← AK24
 USB2_UDP ← AP27
 USB2_UDM ← AP26
 USB2_VBUS_CLMP ← AK26
 USB2_UID ← AK27
 USB1_IBIAS_REXT ← AM26
 USB2_IBIAS_REXT ← AM27
 USB1_VDD_1P8_DECAP ← AL26
 USB2_VDD_1P8_DECAP ← AL27