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VDD1 VDD2 VDDA VREFH VREFL VBAT

7	ADC0_DP0					VREF_OUT/CMP1_IN5/CMP0_IN5	13
8	ADC0_DM0						
						JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/PTA0/UART0_CTS/UART0_COL/FTM0_CH5	17
						JTAG_TDI/EZP_DI/TSI0_CH2/PTA1/UART0_RX/FTM0_CH6	18
						JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/PTA2/UART0_TX/FTM0_CH7	19
6	VREGIN					JTAG_TMS/SWD_DIO/TSI0_CH4/PTA3/UART0_RTS/FTM0_CH0	20
5	VOUT33					NMI/EZP_CS/TSI0_CH5/PTA4/LLWU_P3/FTM0_CH1	21
						EXTAL0/PTA18/FTM0_FLT2/FTM_CLKIN0	24
						XTAL0/PTA19/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	25
3	USB0_DP					ADC0_SE8/TSI0_CH0/PTB0/LLWU_P5/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	27
4	USB0_DM					ADC0_SE9/TSI0_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	28
						ADC0_SE12/TSI0_CH7/PTB2/I2C0_SCL/UART0_RTS/FTM0_FLT3	29
						ADC0_SE13/TSI0_CH8/PTB3/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0	30
						TSI0_CH9/PTB16/UART0_RX/EWM_IN	31
						TSI0_CH10/PTB17/UART0_TX/EWM_OUT	32
26	RESET					ADC0_SE14/TSI0_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG	33
						ADC0_SE15/TSI0_CH14/PTC1/LLWU_P6/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0	34
						ADC0_SE4B/CMP1_IN0/TSI0_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS	35
						CMP1_IN1/PTC3/LLWU_P7/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK	36
						PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT	37
15	EXTAL32					PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT	38
14	XTAL32					CMP0_IN0/PTC6/LLWU_P10/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK	39
						CMP0_IN1/PTC7/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS	40
						PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS	41
						ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS	42
						PTD2/LLWU_P13/SPI0_SOUT/UART2_RX	43
						PTD3/SPI0_SIN/UART2_TX	44
						PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN	45
						ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT	46
						ADC0_SE7B/PTD6/LLWU_P15/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0	47
						PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	48

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VSS1 VSS2 VSSA EXP_PAD

<Value>