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7 38 50 60 70 17

22	DAC0_OUT/CMP1_IN3/ADC0_SE23	VDD1	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14	55
14		VDD2	PTC1/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13	56
13	PGA0_DM/ADC0_DM0/ADC1_DM3	VDD3	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12	57
16	PGA0_DP/ADC0_DP0/ADC1_DP3	VDD4	PTC3/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT	58
15	PGA1_DM/ADC1_DM0/ADC0_DM3	VDD5	PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	61
	PGA1_DP/ADC1_DP0/ADC0_DP3	VDDA	PTC5/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT	62
26			PTC6/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/FB_AD9	63
27	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/FTM0_CH5		PTC7/CMP0_IN1/SPI0_SIN/FB_AD8	64
28	PTA1/JTAG_TDI/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6		PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/I2S0_CLKIN/FB_AD7	65
29	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7		PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0	66
30	PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0		PTC10/ADC1_SE6B/CMP0_IN4/I2C1_SCL/I2S0_RX_FS/FB_AD5	67
31	PTA4/NMI/EZP_CS/TSI0_CH5/FTM0_CH1		PTC11/ADC1_SE7B/I2C1_SDA/I2S0_RXD/FB_RW	68
32	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST		PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16_BLS15_8	71
33	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA		PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24_BLS7_0	72
34	PTA13/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB			
35	PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK		PTD0/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	73
36	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD		PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0	74
37	PTA16/SPI0_SOUT/UART0_CTS/I2S0_RX_FS		PTD2/SPI0_SOUT/UART2_RX/FB_AD4	75
40	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK/I2S0_CLKIN		PTD3/SPI0_SIN/UART2_TX/FB_AD3	76
41	PTA18/EXTAL/FTM0_FLT2/FTM_CLKIN0		PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	77
	PTA19/XTAL/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1		PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT	78
43	PTB0/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA		PTD6/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	79
44	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB		PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	80
45	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3			
46	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/FTM0_FLT0		PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	1
47	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1		PTE1/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL	2
48	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2		PTE2/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	3
51	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN		PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD	4
52	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT		PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3	5
53	PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA		PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	6
54	PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB			
42	RESET		VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	21
10	USB0_DM			
9	USB0_DP		VBAT	25
24	EXTAL32		VOUT33	11
23	XTAL32		VREFH	18
			VREFL	19
			VREGIN	12

<Value>

8 VSS1 39 VSS2 49 VSS3 59 VSS4 69 VSS5 20 VSSA