



	R_0_ETC0/DSPI_2_SCK/EIRQ0	F0/GPIO80/FLEXPWM_0_A1/ETIMER_0_ETC2/EIRQ28	D7
	R_0_ETC1/DSPI_2_SOUT/EIRQ1	F3/GPIO83/DSPI_0_CS6	B5
	R_0_ETC2/FLEXPWM_0_A3/DSPI_2_SIN/MC_RGM_ABS0/EIRQ2	F4/GPIO84/NPC_MDO3	D2
K17	A3/GPIO3/ETIMER_0_ETC3/DSPI_2_CS0/FLEXPWM_0_B3/MC_RGM_ABS2/EIRQ3	F5/GPIO85/NPC_MDO2	D1
C16	A4/GPIO4/ETIMER_1_ETC0/DSPI_2_CS1/ETIMER_0_ETC4/MC_RGM_FAB/EIRQ4	F6/GPIO86/NPC_MDO1	E2
H4	A5/GPIO5/DSPI_1_CS0/ETIMER_1_ETC5/DSPI_0_CS7/EIRQ5	F7/GPIO87/NPC_MCKO	J1
G4	A6/GPIO6/DSPI_1_SCK/EIRQ6	F8/GPIO88/NPC_MSE01	K2
F3	A7/GPIO7/DSPI_1_SOUT/EIRQ7	F9/GPIO89/NPC_MSE00	K1
F4	A8/GPIO8/DSPI_1_SIN/EIRQ8	F10/GPIO90/NPC_EVT0	L1
B6	A9/GPIO9/DSPI_2_CS1/FLEXPWM_0_B3/FLEXPWM_0_FAULT0	F11/GPIO91/NPC_EVT1	L2
A13	A10/GPIO10/DSPI_2_CS0/FLEXPWM_0_B0/FLEXPWM_0_X2/EIRQ9	F12/GPIO92/ETIMER_1_ETC3/EIRQ30	C17
D11	A11/GPIO11/DSPI_2_SCK/FLEXPWM_0_A0/FLEXPWM_0_A2/EIRQ10	F13/GPIO93/ETIMER_1_ETC4/EIRQ31	B14
A10	A12/GPIO12/DSPI_2_SOUT/FLEXPWM_0_A2/FLEXPWM_0_B2/EIRQ11	F14/GPIO94/LINFLEXD_1_TXD	C13
C6	A13/GPIO13/FLEXPWM_0_B2/DSPI_2_SIN/FLEXPWM_0_FAULT0/EIRQ12	F15/GPIO95/LINFLEXD_1_RXD	D13
B4	A14/GPIO14/FLEXCAN_1_TXD/ETIMER_1_ETC4/EIRQ13		
D3	A15/GPIO15/ETIMER_1_ETC5/FLEXCAN_1_RXD/FLEXCAN_0_RXD/EIRQ14		
B15	B0/GPIO16/FLEXCAN_0_TXD/ETIMER_1_ETC2/SSCM_DEBUG0/EIRQ15	FCCU_F0	R2
C14	B1/GPIO17/ETIMER_1_ETC3/SSCM_DEBUG1/FLEXCAN_0_RXD/FLEXCAN_1_RXD/EIRQ16	FCCU_F1	C4
A14	B2/GPIO18/LINFLEXD_0_TXD/SSCM_DEBUG2/EIRQ17	G2/GPIO98/FLEXPWM_0_X2/DSPI_1_CS1	E16
B13	B3/GPIO19/SSCM_DEBUG3/LINFLEXD_0_RXD	G3/GPIO99/FLEXPWM_0_A2/ETIMER_0_ETC4	D17
L17	B4/GPIO20/JTAGC_TDO	G4/GPIO100/FLEXPWM_0_B2/ETIMER_0_ETC5	F17
M15	B5/GPIO21/JTAGC_TDI	G5/GPIO101/FLEXPWM_0_X3/DSPI_2_CS3	N17
B3	B6/GPIO22/MC_CGM_CLK_OUT/DSPI_2_CS2/EIRQ18	G6/GPIO102/FLEXPWM_0_A3	G17
R5	B7/GPIO23/LINFLEXD_0_RXD/ADC_0_AN0	G7/GPIO103/FLEXPWM_0_B3	P17
P7	B8/GPIO24/ETIMER_0_ETC5/ADC_0_AN1	G8/GPIO104/FLEXRAY_DBG0/DSPI_0_CS1/DSPI_0_CS1/FLEXPWM_0_FAULT0/EIRQ21	P16
U7	B9/GPIO25/ADC0_1_AN11	G9/GPIO105/FLEXRAY_DBG1/DSPI_1_CS1/FLEXPWM_0_FAULT1/EIRQ29	R17
R8	B10/GPIO26/ADC0_1_AN12	G10/GPIO106/FLEXRAY_DBG2/DSPI_2_CS3/FLEXPWM_0_FAULT2	P15
T8	B11/GPIO27/ADC0_1_AN13	G11/GPIO107/FLEXRAY_DBG3/FLEXPWM_0_FAULT3	U15
U8	B12/GPIO28/ADC0_1_AN14	G12/GPIO108/NPC_MDO11	F2
R10	B13/GPIO29/LINFLEXD_1_RXD/ADC_1_AN0	G13/GPIO109/NPC_MDO10	H1
P11	B14/GPIO30/ETIMER_0_ETC4/EIRQ19/ADC_1_AN1	G14/GPIO110/NPC_MDO9	A6
R11	B15/GPIO31/EIRQ20/ADC_1_AN2	G15/GPIO111/NPC_MDO8	J2
R12	C0/GPIO32/ADC_1_AN3	H0/GPIO112/NPC_MDO7	A5
T4	C1/GPIO33/ADC_0_AN2	H1/GPIO113/NPC_MDO6	F1
U5	C2/GPIO34/ADC_0_AN3	H2/GPIO114/NPC_MDO5	A4
H3	C4/GPIO36/DSPI_0_CS0/FLEXPWM_0_X1/SSCM_DEBUG4/EIRQ22	H3/GPIO115/NPC_MDO4	G1
G3	C5/GPIO37/DSPI_0_SCK/SSCM_DEBUG5/FLEXPWM_0_FAULT3/EIRQ23	H4/GPIO116/FLEXPWM_1_X0/ETIMER_2_ETC0	L16
D4	C6/GPIO38/DSPI_0_SOUT/FLEXPWM_0_B1/SSCM_DEBUG6/EIRQ24	H5/GPIO117/FLEXPWM_1_A0/DSPI_0_CS4	M17
K4	C7/GPIO39/FLEXPWM_0_A1/SSCM_DEBUG7/DSPI_0_SIN	H6/GPIO118/FLEXPWM_1_B0/DSPI_0_CS5	H17
A15	C10/GPIO42/DSPI_2_CS2/FLEXPWM_0_A3/FLEXPWM_0_FAULT1	H7/GPIO119/FLEXPWM_1_X1/ETIMER_2_ETC1	K16
M14	C11/GPIO43/ETIMER_0_ETC4/DSPI_2_CS2	H8/GPIO120/FLEXPWM_1_A1/DSPI_0_CS6	K15
N15	C12/GPIO44/ETIMER_0_ETC5/DSPI_2_CS3	H9/GPIO121/FLEXPWM_1_B1/DSPI_0_CS7	G16
F15	C13/GPIO45/ETIMER_1_ETC1/CTU_0_EXT_IN/FLEXPWM_0_EXT_SYNC	H10/GPIO122/FLEXPWM_1_X2/ETIMER_2_ETC2	A11
E15	C14/GPIO46/ETIMER_1_ETC2/CTU_0_EXT_TGR	H11/GPIO123/FLEXPWM_1_A2	C11
A8	C15/GPIO47/FLEXRAY_CA_TR_EN/ETIMER_1_ETC0/FLEXPWM_0_A1/CTU_0_EXT_IN/FLEXPWM_0_EXT_SYNC	H12/GPIO124/FLEXPWM_1_B2	B10
B8	D0/GPIO48/FLEXRAY_CA_TX/ETIMER_1_ETC1/FLEXPWM_0_B1	H13/GPIO125/FLEXPWM_1_X3/ETIMER_2_ETC3	G15
E3	D1/GPIO49/ETIMER_1_ETC2/CTU_0_EXT_TGR/FLEXRAY_CA_RX	H14/GPIO126/FLEXPWM_1_A3/ETIMER_2_ETC4	A12
C5	D2/GPIO50/ETIMER_1_ETC3/FLEXPWM_0_X3/FLEXRAY_CB_RX	H15/GPIO127/FLEXPWM_1_B3/ETIMER_2_ETC5	J17
A7	D3/GPIO51/FLEXRAY_CB_TX/ETIMER_1_ETC4/FLEXPWM_0_A3		
B7	D4/GPIO52/FLEXRAY_CB_TR_EN/ETIMER_1_ETC5/FLEXPWM_0_B3		
N3	D5/GPIO53/DSPI_0_CS3/FLEXPWM_0_FAULT2	10/GPIO128/ETIMER_2_ETC0/DSPI_0_CS4/FLEXPWM_1_FAULT0	C9
P3	D6/GPIO54/DSPI_0_CS2/FLEXPWM_0_X3/FLEXPWM_0_FAULT1	11/GPIO129/ETIMER_2_ETC1/DSPI_0_CS5/FLEXPWM_1_FAULT1	C12
R4	D7/GPIO55/DSPI_1_CS3/DSPI_0_CS4/SWG_AO	12/GPIO130/ETIMER_2_ETC2/DSPI_0_CS6/FLEXPWM_1_FAULT2	F16
M3	D8/GPIO56/DSPI_1_CS2/ETIMER_1_ETC4/DSPI_0_CS5/FLEXPWM_0_FAULT3	13/GPIO131/ETIMER_2_ETC3/DSPI_0_CS7/CTU_0_EXT_TGR/FLEXPWM_1_FAULT3	E17
L3	D9/GPIO57/FLEXPWM_0_X0/LINFLEXD_1_TXD		
T15	D10/GPIO58/FLEXPWM_0_A0/ETIMER_0_ETC0	NC_CUT1/RDY_CUT2/GPIO132_CUT2	K3
R16	D11/GPIO59/FLEXPWM_0_B0/ETIMER_0_ETC1		
G14	D12/GPIO60/FLEXPWM_0_X1/LINFLEXD_1_RXD	BCTRL	R13
D16	D14/GPIO62/FLEXPWM_0_B1/ETIMER_0_ETC3	JCOMP	C10
T13	E0/GPIO64/ADC_1_AN5	MDC0	E1
U6	E2/GPIO66/ADC_0_AN5	TMS	M16
U4	E4/GPIO68/ADC_0_AN7	NMI	E4
T5	E5/GPIO69/ADC_0_AN8	RESET	P2
R6	E6/GPIO70/ADC_0_AN4		
T6	E7/GPIO71/ADC_0_AN6		
T10	E9/GPIO73/ADC_1_AN7		
T11	E10/GPIO74/ADC_1_AN8		
U11	E11/GPIO75/ADC_1_AN4	TCK	L15
T12	E12/GPIO76/ADC_1_AN6		
D12	E13/GPIO77/ETIMER_0_ETC5/DSPI_2_CS3/EIRQ25		
B12	E14/GPIO78/ETIMER_1_ETC5/EIRQ26	XTALIN	N1
B11	E15/GPIO79/DSPI_0_CS1/EIRQ27	XTALOUT	R1

<Value>



T7	VSS_HV_ADR0	VDD_HV_AI
T9	VSS_HV_ADR1	VDD_HV_AI
U10	VSS_HV_ADV	VDD_HV_REG
P1	VSS_HV_ADV	VDD_HV_REG
J16	VSS_HV_ADV	VDD_HV_REG
D15	VSS_HV_FL	VDD_HV_REG
N4	VSS_HV_FL	VDD_HV_REG_2_1
A1	VSS_HV_FL	VDD_HV_REG_2_2
A2	VSS_HV_FL	VDD_HV_REG_2_2
A16	VSS_HV_FL	VDD_HV_REG_2_2
A17	VSS_HV_FL	VDD_HV_REG_2_2
B1	VSS_HV_FL	VDD_HV_REG_2_2
B2	VSS_HV_FL	VDD_HV_REG_2_2
B9	VSS_HV_FL	VDD_HV_REG_2_2
B17	VSS_HV_FL	VDD_HV_REG_2_2
C3	VSS_HV_FL	VDD_HV_REG_2_2
C15	VSS_HV_FL	VDD_HV_REG_2_2
D9	VSS_HV_FL	VDD_HV_REG_2_2
H2	VSS_HV_FL	VDD_HV_REG_2_2
N2	VSS_HV_FL	VDD_HV_REG_2_2
P9	VSS_HV_FL	VDD_HV_REG_2_2
R3	VSS_HV_FL	VDD_HV_REG_2_2
R15	VSS_HV_FL	VDD_HV_REG_2_2
T1	VSS_HV_FL	VDD_HV_REG_2_2
T17	VSS_HV_FL	VDD_HV_REG_2_2
U1	VSS_HV_FL	VDD_HV_REG_2_2
U2	VSS_HV_FL	VDD_HV_REG_2_2
U16	VSS_HV_FL	VDD_HV_REG_2_2
U17	VSS_HV_FL	VDD_HV_REG_2_2
D5	VSS_LV_CORE_RING1	VDD_LV_CORE_RING1
G7	VSS_LV_CORE_RING2	VDD_LV_CORE_RING2
G8	VSS_LV_CORE_RING3	VDD_LV_CORE_RING3
G9	VSS_LV_CORE_RING4	VDD_LV_CORE_RING4
G10	VSS_LV_CORE_RING5	VDD_LV_CORE_RING5
G11	VSS_LV_CORE_RING6	VDD_LV_CORE_RING6
P6	VSS_LV_CORE_RING7	VDD_LV_CORE_RING7
P13	VSS_LV_CORE_RING8	VDD_LV_CORE_RING8
H7	VSS_LV1	VDD_LV1
H8	VSS_LV2	VDD_LV2
H9	VSS_LV3	VDD_LV3
H10	VSS_LV4	VDD_LV4
H11	VSS_LV5	VDD_LV5
H14	VSS_LV6	VDD_LV6
K7	VSS_LV7	VDD_LV7
K8	VSS_LV8	VDD_LV8
K9	VSS_LV9	VDD_LV9
K10	VSS_LV10	VDD_LV10
K11	VSS_LV11	VDD_LV11
J7	VSS_LV12	VDD_LV12
J8	VSS_LV13	VDD_LV13
J9	VSS_LV14	VDD_LV14
J10	VSS_LV15	VDD_LV15
J11	VSS_LV16	VDD_LV16
L10	VSS_LV17	VDD_LV17
L11	VSS_LV18	VDD_LV18
L7	VSS_LV19	VDD_LV19
L8	VSS_LV20	VDD_LV20
L9	VSS_LV21	VDD_LV21

U?B

NC_E14
NC_K14
NC_L14
NC_L4
NC_M4
NC_N14
NC_P8
NC_P8
NC_T3
NC_U3
NC_U12
NC_U12
NC_U13
NC_U13
NC_C2
NC_D10
NC_D10
NC_F14

F14
K14
L14
L4
M4
N14
P8
P8
T3
U3
U12
U12
U13
U13
C2
D10
D10
F14

<Value>