

U?

2

VDDXRA/VRH

		PP0/KWP0/ETRIG0/API_EXTCLK/PWM0	9
1	RESET	PP1/KWP1/ETRIG1/ECLKX2/PWM1	10
8	BKGD/MODC	PP2/KWP2/ETRIG2/PWM2	11
7	TEST	PP3/KWP3/ETRIG3/PWM3	12
		PAD0/KWAD0/AN0	17
16	PT0/IOC0/ $\overline{\text{XIRQ}}$	PAD1/KWAD1/AN1	18
15	PT1/IOC1/IRQ	PAD2/KWAD2/AN2	19
14	PT2/IOC2	PAD3/KWAD3/AN3	20
13	PT3/IOC3	PAD4/KWAD4/AN4	21
		PAD5/KWAD5/AN5/ACMPO	22
31	PM0/RXD1/RXCAN	PAD6/KWAD6/AN6/ACMPP	23
32	PM1/TXD1/TXCAN	PAD7/KWAD7/AN7/ACMPM	24
		PS0/RXD0	25
6	PE1/XTAL	PS1/TXD0	26
4	PE0/EXTAL	PS4/PWM4/MISO0	27
		PS5/IOC4/MOSI0	28
		PS6/IOC5/SCK0	29
		PS7/API_EXTCLK/ECLK/PWM5/SS0	30
	VSSXA		
	VSS		
		<Value>	
	3		
	5		