X-S32K148EVBQ144/Q176

C U S T O M E R E V B

Table of Contents

Revisions

Rev	Description	Designer	Date	Approved
X4	Schematic	J.Sanchez		TBD
A	First Release			

C A U T I O N :

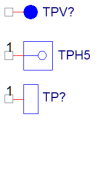
This schematic is provided for reference purposes only. As such, NXP does not make any warranty, implied or otherwise, as to the suitability of circuit design or component selection (type or value) used in these schematics for hardware design using the NXP S32K family of Microprocessors. Customers using any part of these schematics as a basis for hardware design, do so at their own risk and Freescale does not assume any liability for such a hardware design.

3 Different test points used in design:

TPVx - Through Hole Pad small

TPHx - Through Hile Pad Large (for standard 0.1" header). Also used on IO Matrix (IOMx)

TPX - Surface Mount Wire Loop



Notes:

- All components and board processes are to be ROHS compliant
- All connectors and headers are denoted Jx/Px and are 2.54mm pitch unless otherwise stated
- All jumpers are denoted Jx. Jumpers are 2mm pitch
- Jumper default positions are shown in the schematics. For 3 way jumpers, default is always posn 1-2.
  2 Pin jumpers generally have the "source" on pin 1.
- All switches are denoted SWx
- All test points (SMT wire loop style) are denoted TPx
- Test point Vias (just through hole pads) are denoted TPVx

Signals (ports) have not been routed via busses as this makes it harder to determine where each signal goes.

User notes are given throughtout the schematics.

Specific PCB LAYOUT notes are detailed in ITALICS

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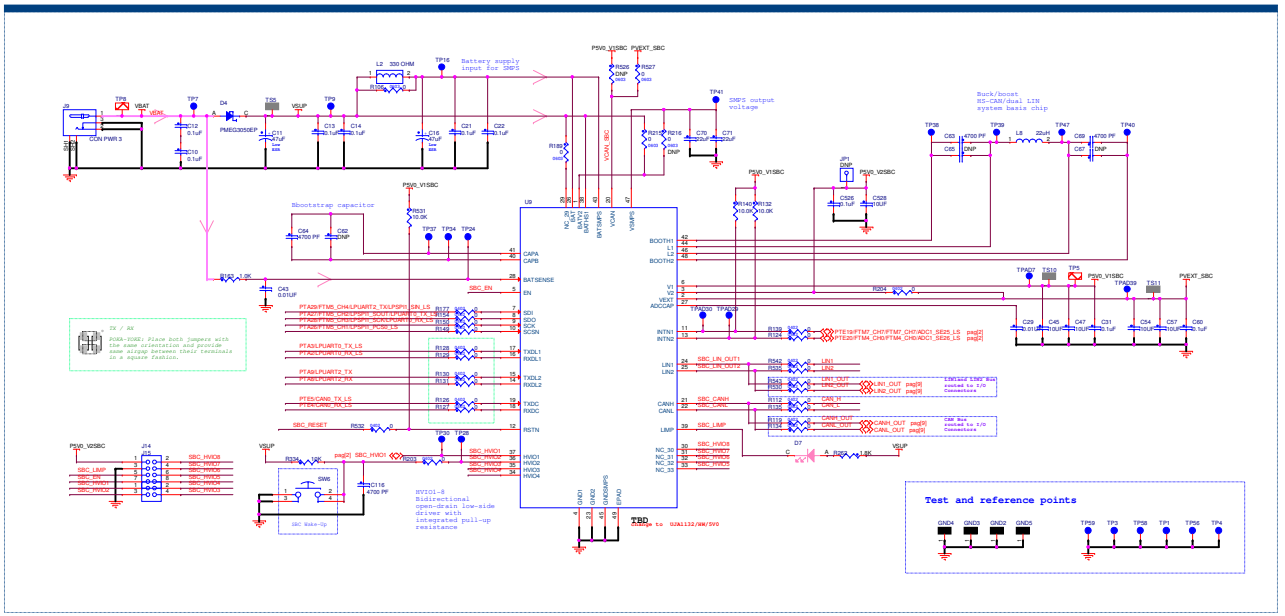
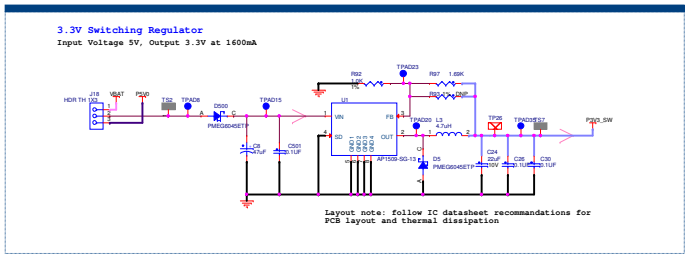
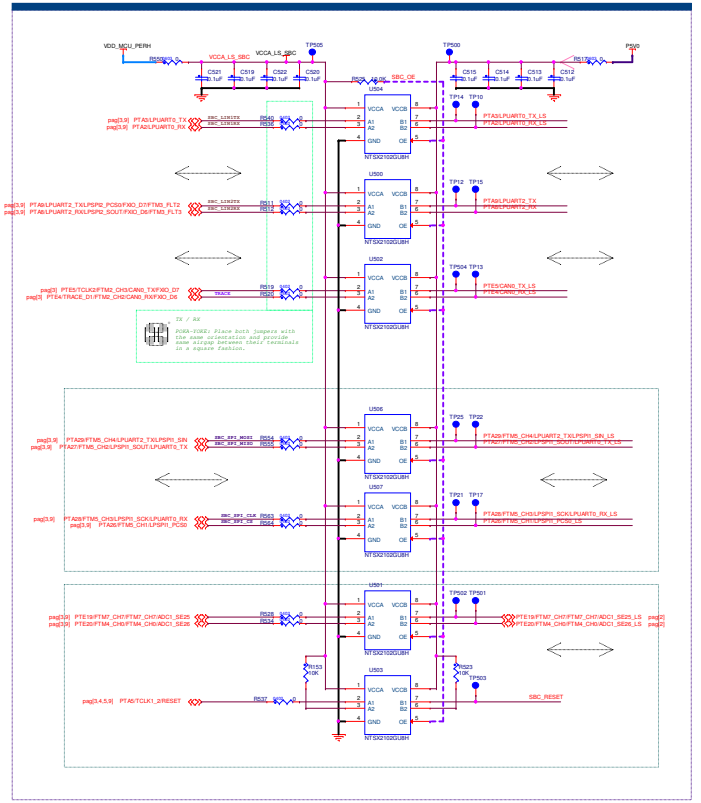
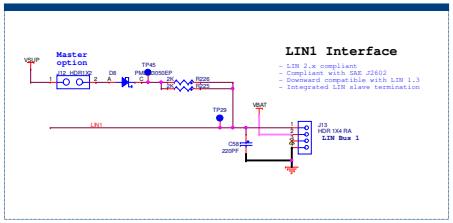
ICAP Classification: CP: ___ IUO: X PUBI: ___

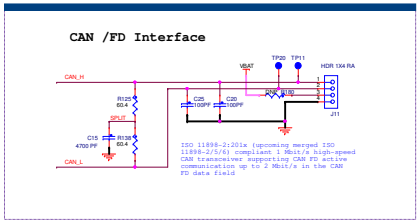
Designer: Jesus Sanchez	Drawing Title: X-S32K148EVB-Q1XX		
Drawn by: Jesus Sanchez	Page Title: TITLE		
Approved: Juan Romero	Size B	Document Number SCH-29642 PDF: SPF-29642	Rev A
	Date: Thursday, April 27, 2017	Sheet 1 of 11	

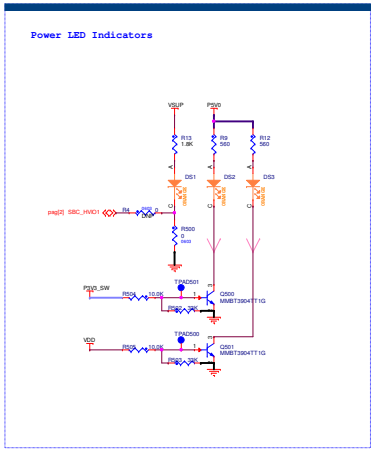


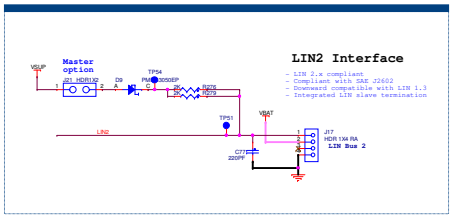


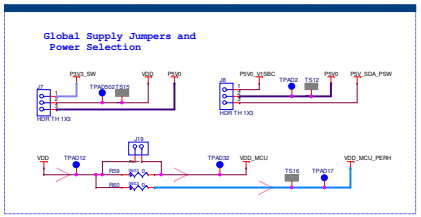












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Drawn by: Jesus Sanchez	Page Title:	I/O HEADERS
Approved: Juan Romero	Size: D	Document Number: SCH-29642 PDF: SPF-29642 Rev: A
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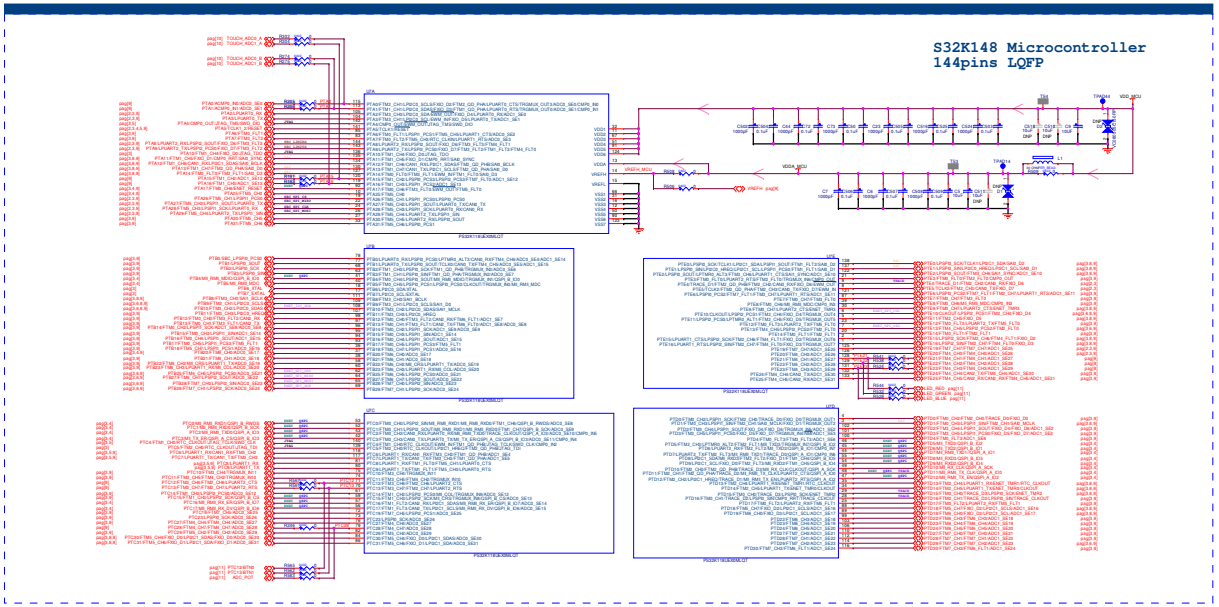

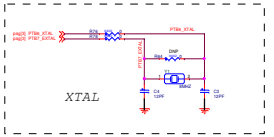

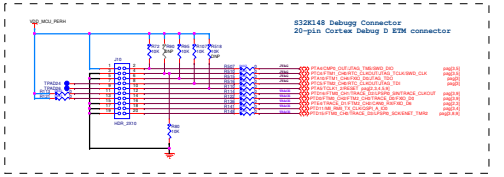

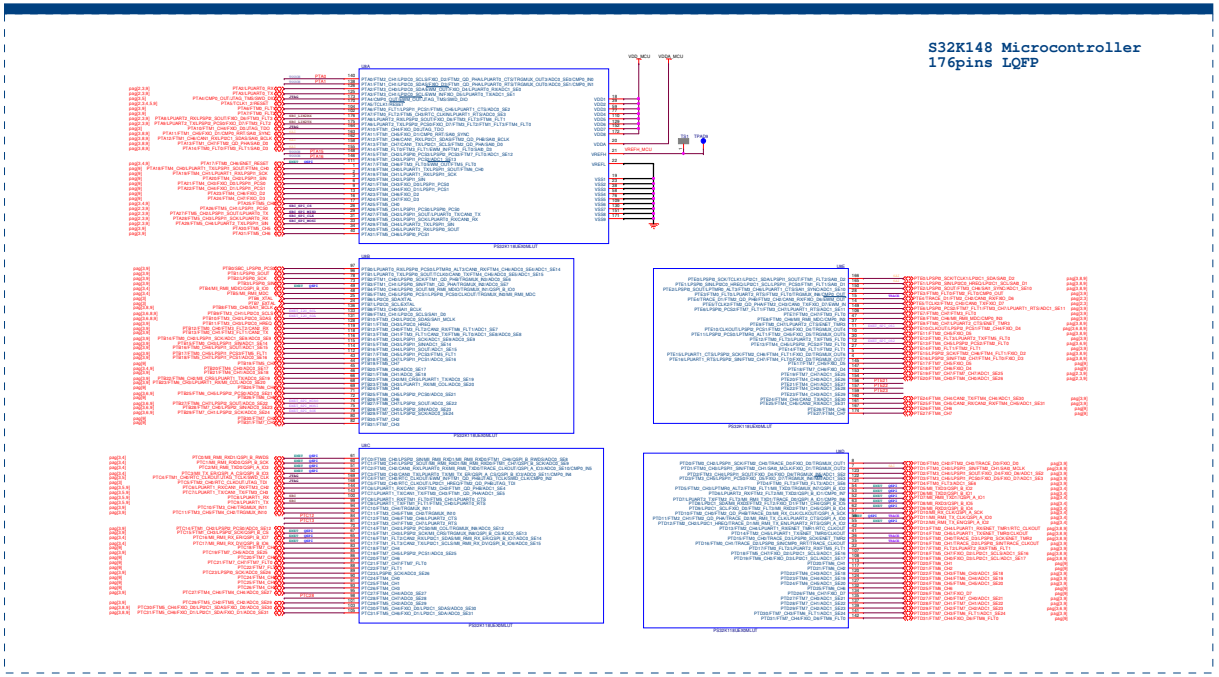

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Drawn by: Jose Sanchez	Page Title:	IO HEADERS	
Approved: Juan Ramos	Size: A	Document Number: SCH-29843 PDF: SPF-29843	Rev: A
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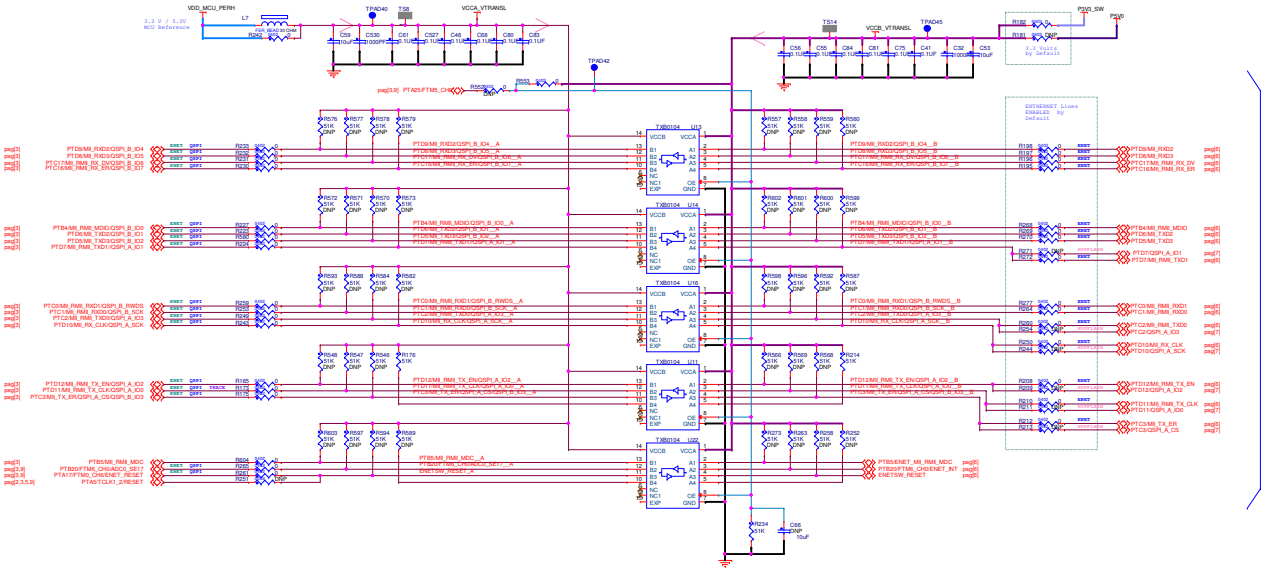
S32K148
MCU SIGNALS



HyperFLASH &
Ethernet Connector

Designer: J.SANCHEZ	Drawing Title: X-S32K148EVB-Q1XX	
Drawn by: Jesus Sanchez	Page Title: 06. LEVELTRANS_MUXDEMUX	
Approved: <Approver>	Size: D	Document Number: SCH-29642 PDF: SPF-29642 Rev: A
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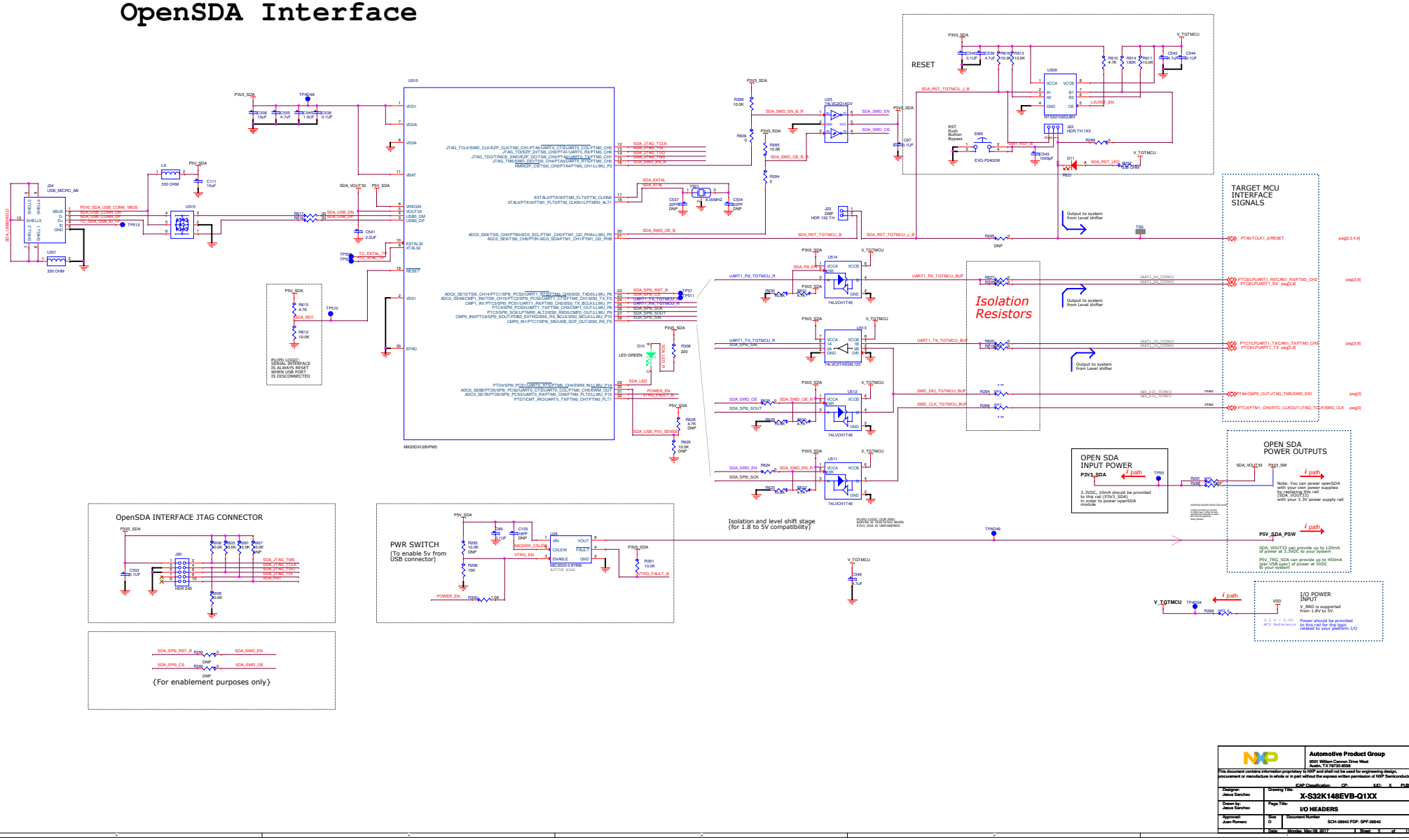
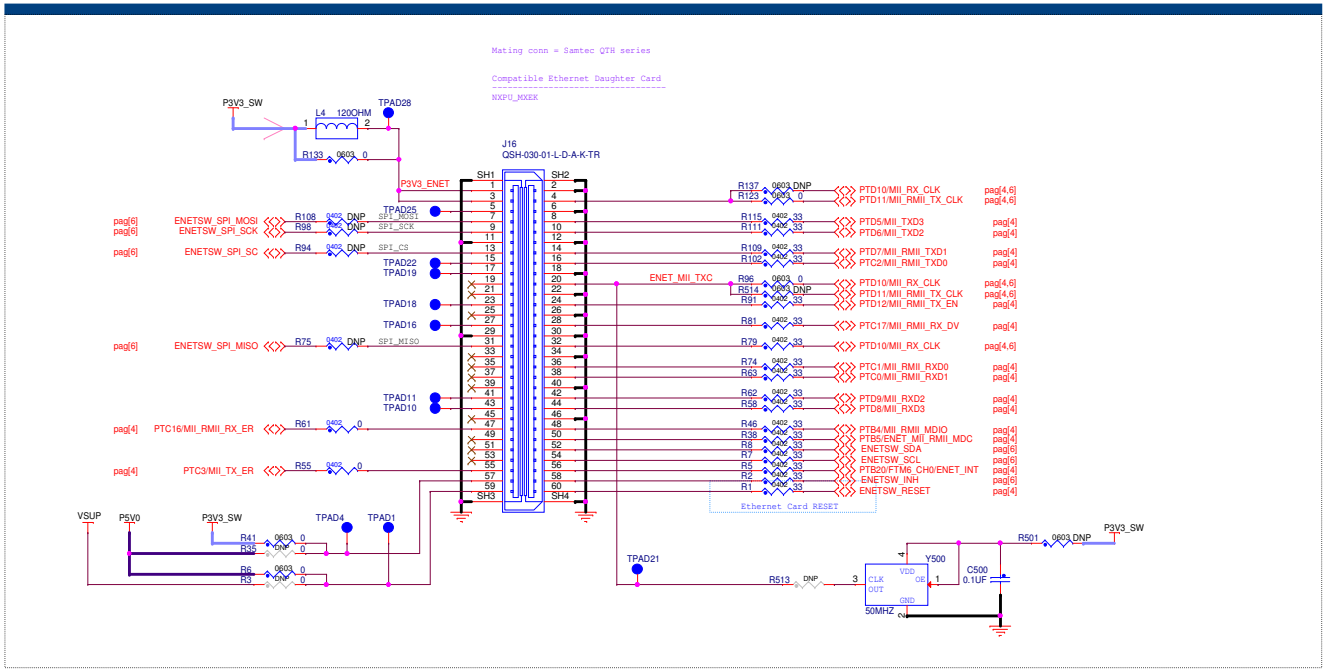
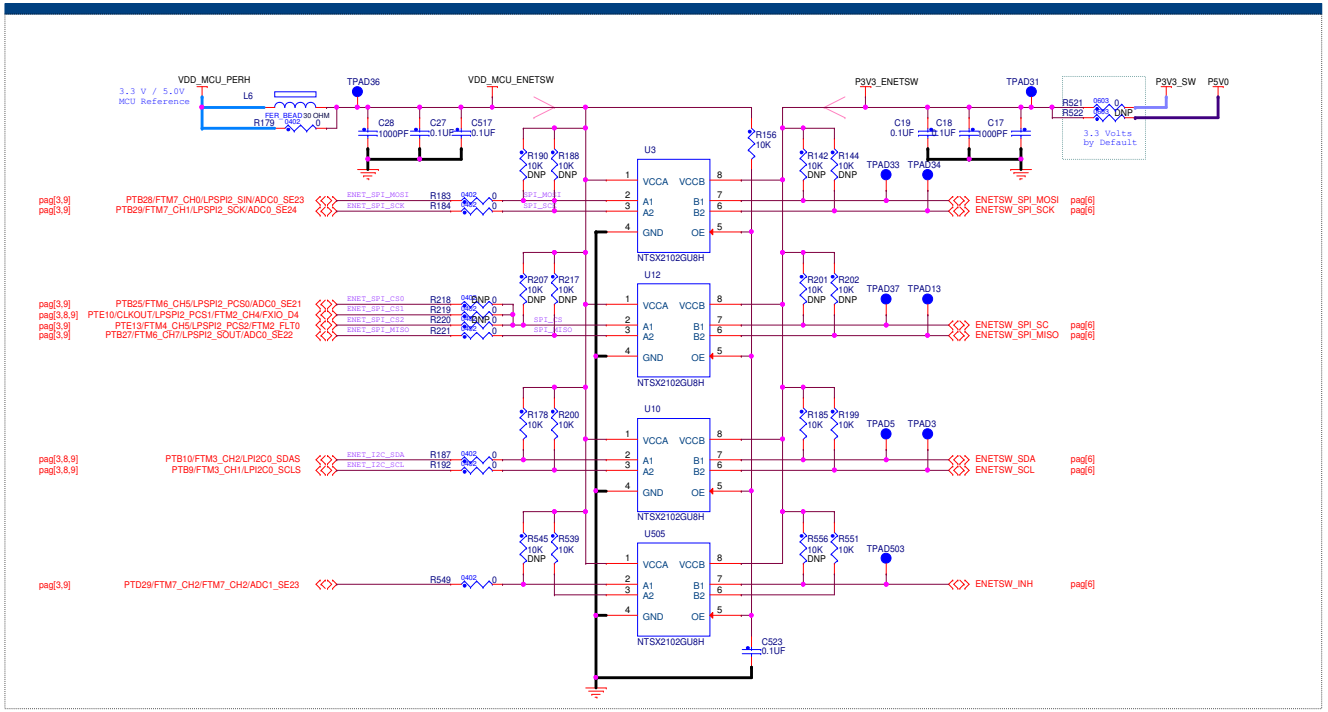
OpenSDA Interface







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Designer: J.SANCHEZ	Drawing Title: **X-S32K148EVB-Q1XX**
Drawn by: Jesus Sanchez	Page Title: **07. ETHERNET**
Approved: <Approver>	Size C / Document Number SCH-29642 PDF: SPF-29642 / Rev A
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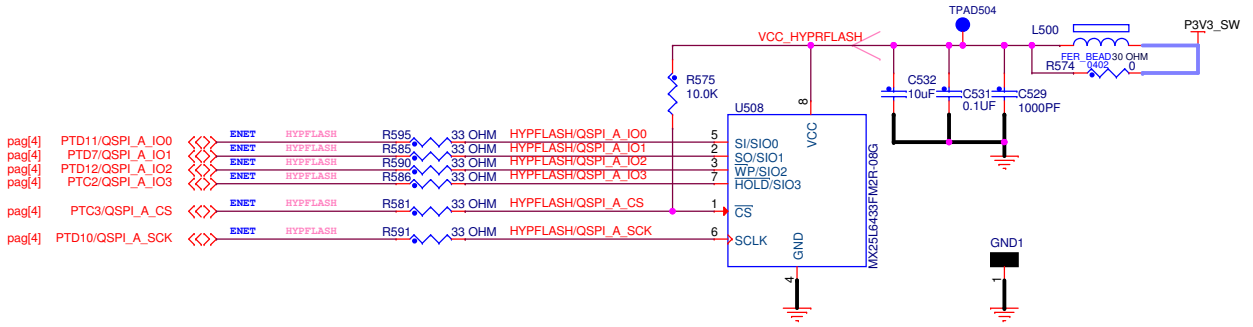
CMOS FLASH Memory 64M-BIT



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Approved: <Approver>	Size B	Document Number SCH-29642 PDF: SPF-29642	Rev A
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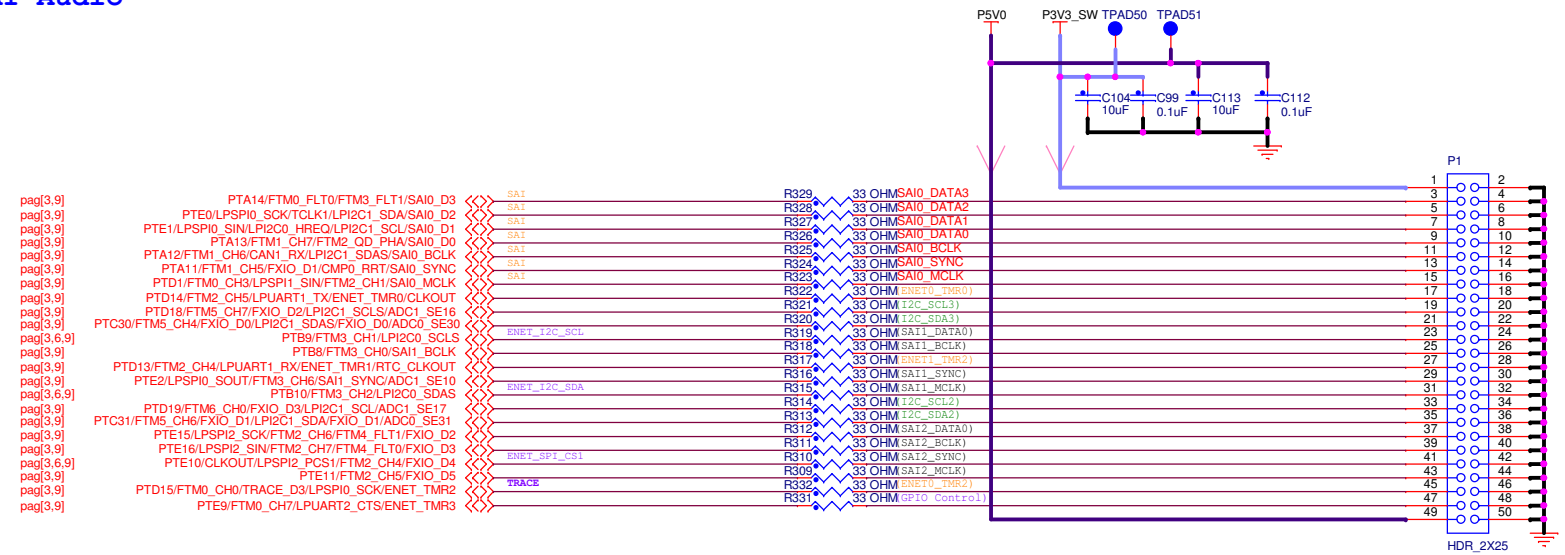
SAI Audio



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Drawn by: Jesus Sanchez	Page Title: **10. SAI AUDIO**		
Approved: <Approver>	Size B	Document Number SCH-29559 PDF: SPF-29559	Rev A
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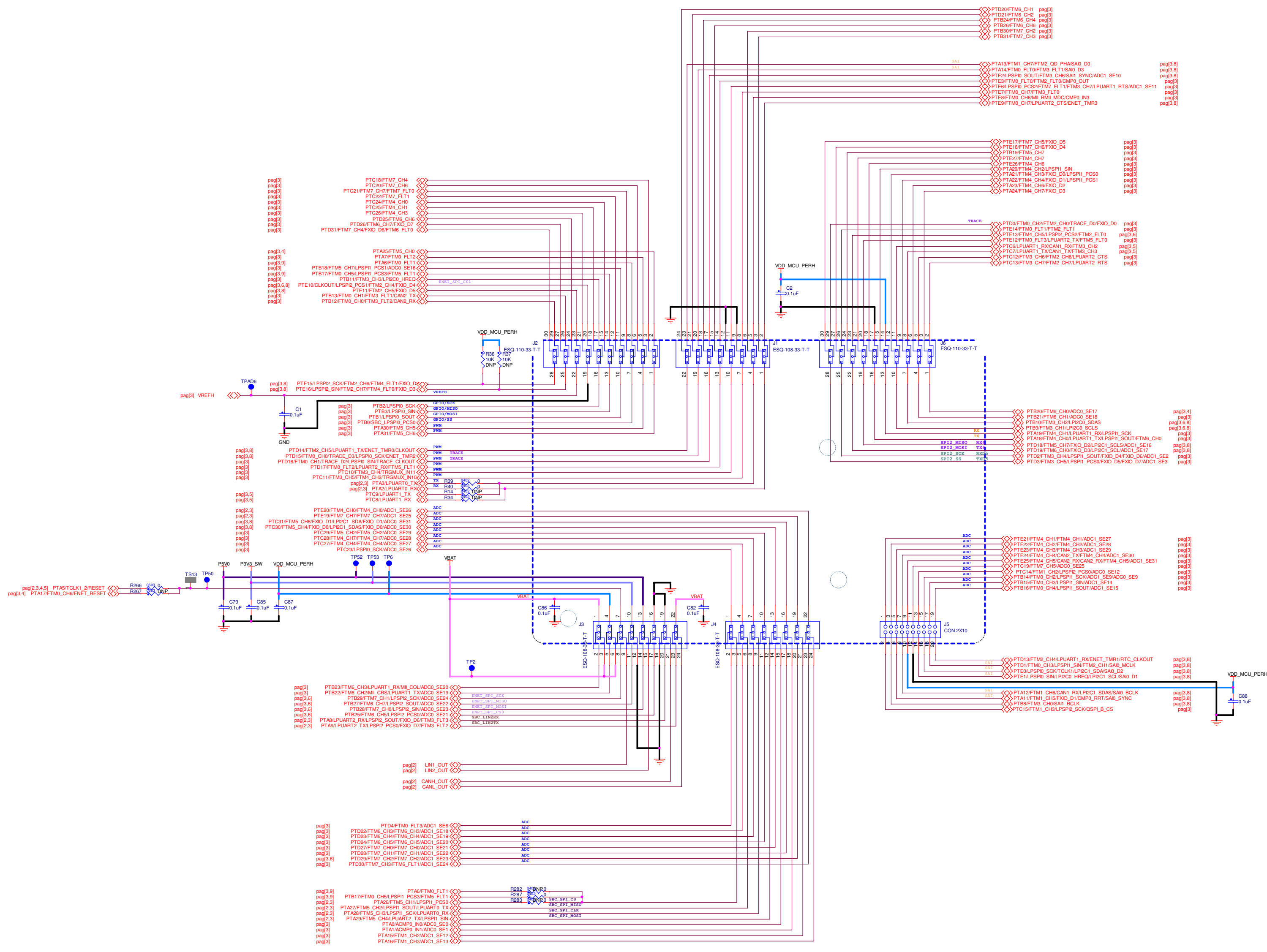

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Approved: Juan Romero	Size D	Document Number SCH-29642 PDF: SPF-29642	Rev A
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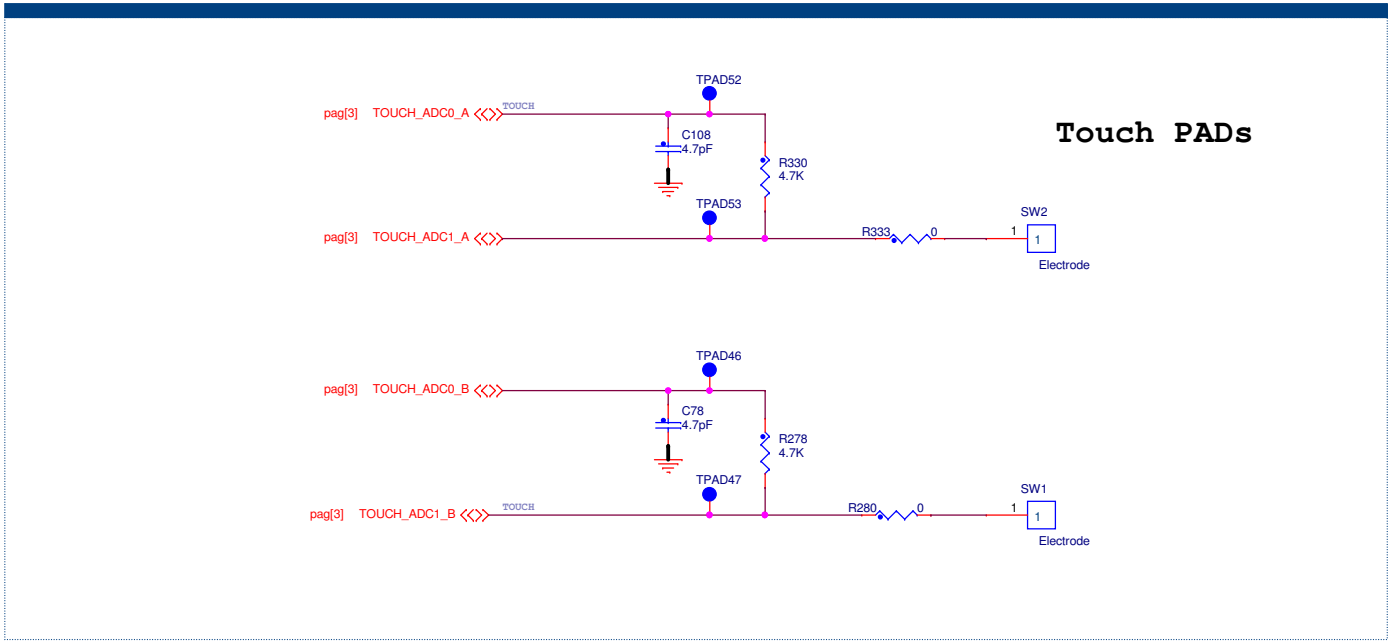

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Drawn by: Jesus Sanchez	Page Title: 11. TOUCHPAD		
Approved: <Approver>	Size B	Document Number SCH-29642 PDF: SPF-29642	Rev A
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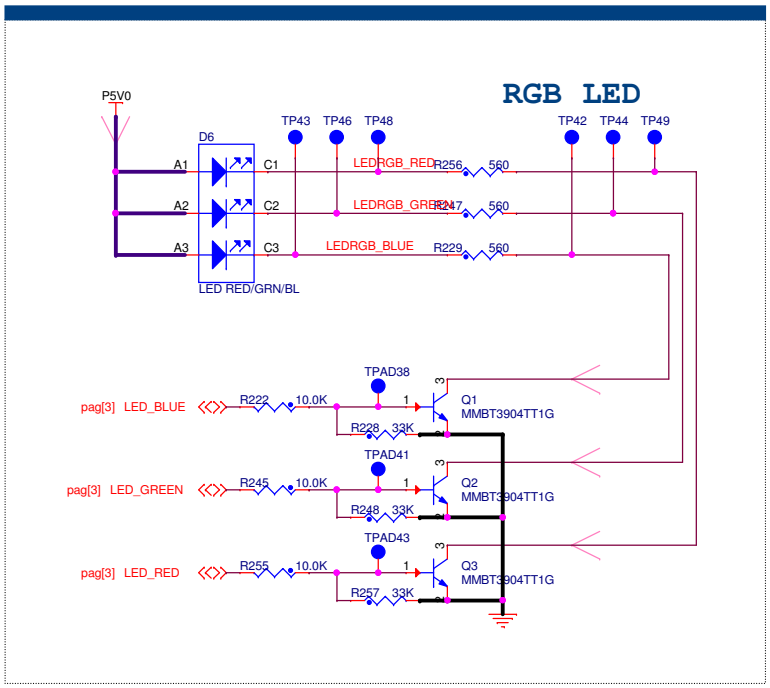

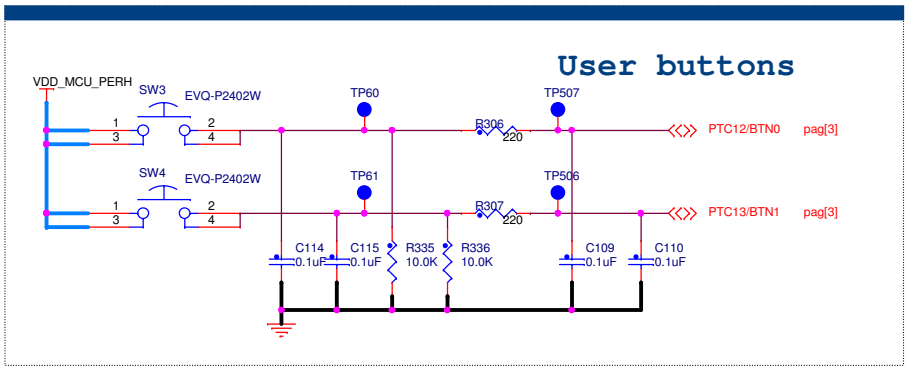

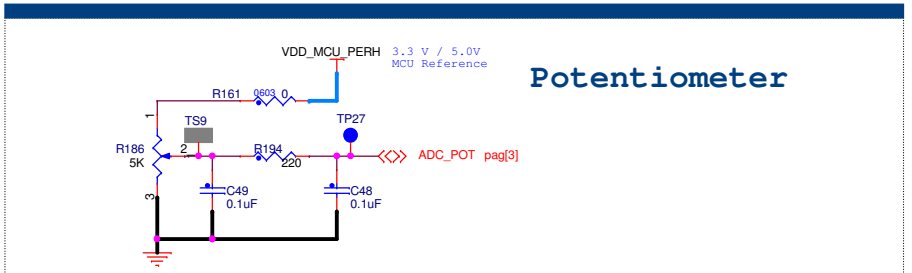

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